THESIS ON
CONTROL OF BUCK CONVERTER BY POLYNOMIAL,
PID AND PD CONTROLLERS.

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This Thesis is presented as a part of our Degree in Master of Science in Electrical Engineering.

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June - 2012
Abstract

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Abstract:

This thesis is an ongoing project of Ericsson with collaboration of Blekinge Institute of Technology [BTH], and Linneaus University [LNU] to compare the functionality and performance of three controllers Polynomial Pole Placement, PID [Proportional Integral Derivative] and PD controller in third order.

This paper presents the state space modeling approach of DC-DC Buck converter. The main aim of this thesis is, by considering the buck converter system of Ericsson BMR450 with the PID, POLYNOMIAL and PD controllers at feedback loop, thus running their Matlab file with their appropriate Simulink block diagram, and comparing the three controllers performance by verifying their appropriate output graphs.

The third order controller design is complicated and response is slow. The second order design is easy and gives better responses than third order Polynomial, PID and PD controllers.

Index Terms:

Buck Converters, DC-DC Converters, PID, PD & POLYNOMIAL Controllers.
Chapter 1. Introduction:
Over the past three to four decades, the conventional use of electrical components is growing very rapidly. So, the competition is inevitable in making the devices as small as possible to satisfy customer needs. In which, the power consumption is one of the important factor for every electrical component to run efficiently.

1.1 Thesis Background:
As the electrical components are packed together, the power conversion mechanisms are changed as linear regulators, which can provide very high quality output voltage and for higher power levels, switching regulators were used. Switching regulators use power electronic semiconductor switches in ON and OFF states, because there is a small power loss in those states [low voltage across a switch in the on state, zero current through a switch in the off state], thus switching regulators can achieve high efficiency energy conversion. The switched-mode power supply [SMPS] is a prominent element in processing the electrical power, in power electronics field [1].

A Buck converter is a DC-DC voltage step down converter. Because of its high efficiency over a wide range of load current, this device is one of the extensively used DC-DC converter topologies in power management systems. For these requirements, the Buck converters are used for fast load line transient response and also for high efficiency over a large load current range [2]. For example, in a computer motherboard the load voltage can be regulated to 5v or 3.3v to satisfy the requirements of Integrated circuits [IC] or sub-circuits. Here the voltage is constant unlike the batteries, where the voltage declines after a certain period of operation.

By using the same circuit with use of switched modes different voltage levels can be obtained [3]. The switching will be controlled by Pulse Width Modulator [PWM] signals. It constantly maintains voltage range regardless of the load current variation and the voltage drop of the battery supply [4].
The non-isolated converters were usually employing an inductor, with no dc voltage isolation between the input and the output. In most of the converters usage, dc isolation is not necessary.

1.2 Thesis Aim:
The main objective of this thesis is to design a buck converter controller based on the theory for discrete Polynomial PD and PID Controller and also evaluating their performances, For the evaluated polynomial PD and PID controller can be found in [5], and [6]. The control design should be evaluated by means of accuracy in output voltage level given some different kind of disturbances which are, load current changes, measurement noise and parameter variations.

1.3 Thesis Scopes:
The following are scopes of this thesis:

- Study the operation of Buck Converter.
- Design the mathematical system model.
- Design the Polynomial PD and PID Controller.
- Simulation of Buck Converter and Controller circuit using SIMULINK and in MATLAB, for testing the properties of the system.
- Final conclusion from the simulation result.
- Reference and Appendix.

1.4 Outline of Thesis:
There are 6 chapters, present in this thesis, here First chapter tells about the thesis objective, introduction and scope. The Second chapter explains about the background of the Buck Converter circuit. The mathematical system models of the Buck Converter were built in Third chapter. The Polynomial Pole Placement and PID Controllers design with simulations implemented on Matlab and Simulink were presented in Fourth and Fifth chapters. The Chapter sixth tells about the results and conclusion obtained with future work, and the final part provides the references and appendix used in this thesis work.
Chapter 2. Basic Buck Converter Circuit Topology:

The basic buck converter operates in two modes i.e., switch ON and switch OFF states. Here these two modes are briefly explained with the help of below Figure 1.

![Basic Buck Converter with a resistive load.](image)

Where $V_S$ = voltage source, $D$=diode, $L$= inductor, $C$= capacitor, $R$= resistor and $V_0$= load voltage.

When the switch is closed, the input voltage is connected to the inductor and the load voltage. Thus the energy will be stored as the current increases in the inductor. And the capacitor begins to discharge its voltage. When the switch is open, the inductor discharges its energy to the load resistor, thus decreasing in current will occur in the system gradually.

In this case, the inductor acts as a source to the load to keep the flow of current without interruption in the circuit. The functions of elements in the above Basic Buck Converter are explained below.

### 2.1 Switch:

The switch is basically a power switch or a transistor. At the gate of the transistor, the PWM signal is introduced to control the ON and OFF state of the transistor. During the switch ON, the supply voltage is equal to the sum of the voltages across the inductor and resistor at load. During the switch OFF, the voltage across the inductor is equal to voltage across the resistor at load. By varying the PWM signal between the switch ON and OFF states we can control the average output voltage.
2.2 Inductor:
The inductor main function is to supply power constantly to the load resistor. In OFF state no power is supplied by the voltage source, and thus the inductor acts as a source at this stage by transferring the current to the load as shown in Figure 2.

2.3 Capacitor:
The capacitor reduces the ripple in the output voltage and here the capacitor wave-off output voltage by filtering away the harmonic currents away from the load. Capacitance across the load is very important for reducing the voltage and ripples present at the output of a step-down converter.

2.4 Diode:
When the switch is open [OFF], a current path should be present for the inductor current, this path is provided by the catch diode [freewheeling diode]. The purpose of this diode is not to rectify, but to direct current flow in the circuit and to ensure that there is always a path for the current to flow into the inductor. It is also necessary that this diode should be able to turn off relatively fast. Thus the diode enables the converter to convert stored energy in the inductor to the load.

When the switch closes, the current rises linearly [exponentially if resistance is also present]. When the switch opens, the freewheeling diode causes a linear decrease in current. At steady state we have a saw tooth response with an average value of the current.

To maintain a constant output voltage in normal operation conditions, the feedback loop and control circuitry are carefully nested around the desired circuits and control by pulse-width modulation is necessary for regulating the output.

2.5 States of Operation:
The operation of the DC-DC converter is simple, it works in two states i.e., ON and OFF states. The explanation of ON state is as follows.
2.5.1 ON State (Switch is closed):

During the ON state, the closed switch results in transferring the energy from the source voltage \([V_s]\) to the inductor \([L]\), thus the diode becomes reverse bias. In this state, current through the inductor increases gradually, as shown in Figure 2.

![Figure 2: Buck Converter in ON state.](image)

2.5.2 OFF State (Switch is open):

During the OFF state, the switch is open, now the inductor acts as a source by maintaining constant energy transfer to the resistor at load. At this stage, the diode starts conducting and the current through the circuit decreases linearly as the energy in the inductor discharges as shown in Figure 3.

![Figure 3: Buck Converter in OFF state.](image)

The Figure 4 graph is obtained when the switch is in ON and OFF states and the response of the current across the diode and load, where shown accordingly.
Figure 4: Diode and the load during the two switch states of operation of buck converter, where $i_0$ represents the average current across the load.

2.5.3 Modes of operation:

The buck converter operates in two modes i.e., continuous and discontinuous modes of operation. In continuous mode, the current passing through the inductor doesn’t fall to zero. But in discontinuous mode, the current passing through the inductor reduces to zero during its part of the switching period. This phenomenon exists due to the time and energy required by the load is enough when compared to the complete operating period. In this thesis, we used continuous mode of operation.

2.5.4 Ericsson BMR 450 Features:

This BMR450 is one of the first products developed by Ericsson in digital power [computerizing the DC-DC converters] the other models are BMR 451 and 453, presently it is the main focus of the DC-DC converter area today.
Digital PWM with adaptive dead-time control, precision delay and ramp-up are the main features of BMR450. Other features are listed below.

Input: 4.5-14 V, Output: 20 A/100W.

- Small package 25.65 x 12.9 x 8.2 mm [1.01 x 0.51 x 0.323 in].
- 20 A output current.
- 4.5 V - 14 V input voltage range.
- 0.6 V - 5.5 V output voltage range.
- High efficiency, typ. 96.8 % at half load, 5 V\text{in}, 3.3 V\text{out}.
- 5 million hours MTBF.
- Through hole and surface mount versions.
- PM Bus complaint.
- Voltage/current/temperature monitoring.
- Precision delay and ramp-up.
- Non-linear transient response.
- Wide output voltage adjust function.
- Start up into a pre-biased output safe.
- Output short-circuits protection.
• Over temperature protection.
• ON/OFF inhibit control.
• Output voltage sense.
• ISO 9001/14001 certified supplier.
Chapter 3. Modeling of Buck Converter:

The buck converter circuit is transformed to graph using graph theory as shown below.

![Buck converter model after implementing cut-set method.](image)

Figure 6: Buck converter model after implementing cut-set method.

3.1 Mathematical equations of the buck converter when the switch is in ON state:

To the above Figure 6, Cut-Set method is applied to obtain the tree branch, this is shown in the below Figure 7, in this figure a cut \([i_u]\) is made across the branch [twig], this \(i_u\) direction depends up on the direction of the branch. Now this \(i_u\) cut has the direction, if the link direction [indicated in green color no: 7] is same as the cut direction \([i_u]\) then we can place ‘1’ under number 7 across \(i_u\) in the matrix shown below [Matrix 1].

Similarly if we consider \([iC1]\) which cuts through three links indicated with 6, 7 and 8 arrows were the link indicated with number 6 and 8 has opposite direction as compare with the cut \([iC1]\), so we had written ‘-1’ under numbers 6 and 8 across \(iC1\), where as the link 7 is in the same direction as that of the twig direction, so ‘1’ is placed in the below matrix, if the twig does not cut the link then ‘0’ is placed in the matrix. Like this we had fill the below matrix for all the five cut’s i.e., \([i_u,i_l, i_r,i_C1,i_C2]\).
Figure 7: When Cut-Set method is applied to the graph in Figure 6.

$$\begin{align*}
1 & \quad 2 & \quad 3 & \quad 4 & \quad 5 & \quad 6 & \quad 7 & \quad 8 \\
i_U & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 1 & \quad 0 \\
ic_1 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 1 & \quad 1 & \quad -1 \\
ic_2 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 1 & \quad 0 \\
i_{RL} & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad -1 & \quad 1 & \quad -1 \\
i_{R1} & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 & \quad 0 \\
\end{align*}$$

Matrix 1: The above equation is the result of the cut-set branch direction as shown in the Figure 7.

Where, \(i_U\) = current through the voltage source, \(ic_1\) = current through the capacitor C1, 
\(ic_2\) = current through the capacitor C2, \(i_{RL}\) = current through the resistor RL, 
\(i_{R1}\) = current through the resistor R1,

Now we have to write the matrix for the links [current sources] other than branches, for this we have to consider the KCL [Kirchhoff’s Current Law] as shown below,
Here the loops are drawn according to the directions of the links, and the below matrix is written depending up on the directions of the loops \([L1, L2, L3]\) with reference to the direction of the links \([\text{current source’s}]\) i.e., from the above figure 8, loop L1 direction and the branch direction \([\text{number 3}]\) are same so we kept ‘-1’, and for branches \([\text{number 2 and 5}]\) it is ‘1’ because they are in opposite direction to the loop direction, then for the rest of the branches ‘0’ is assigned \([\text{because loop L1 did not touch the other branches}]\).

And for loop L2, the branches number 1, 2, 4 and 5 are in same direction so we mentioned ‘-1’ and ‘0’ for the remaining branches as mentioned in the below Matrix 2 is built. Same method for L3 loop also.

\[
\begin{pmatrix}
UR2 \\
UL \\
UI
\end{pmatrix}
= \begin{pmatrix}
0 & 1 & -1 & 0 & 1 & 0 & 0 & 0 \\
-1 & -1 & 0 & -1 & -1 & 0 & 0 & 0 \\
0 & 1 & 0 & 1 & 0 & 0 & 0 & 0
\end{pmatrix}
\begin{pmatrix}
iR2 \\
iL \\
iI
\end{pmatrix}
\]

Matrix 2: result of the KCL links direction as shown in the Figure 8.

Where, \(u_{R2}\) in derivative form \(i_{R2} \frac{d}{dt}i_{R2}\) = voltage through the resistor R2,

\(u_L\) in derivative form \(i_L \frac{d}{dt}i_L\) = voltage through the inductor L,

\(u_I\) in derivative form \(i_I \frac{d}{dt}i_I\) = voltage through the current source I,
From the above two Matrices 1 & 2, we get the below Matrix 3 [8x8] by combining both the method as shown below,

\[
\begin{bmatrix}
    i_U \\
    i_{C1} \\
    i_{C2} \\
    i_{RL} \\
    i_{R1} \\
    u_{R2} \\
    u_L \\
    u_I \\
\end{bmatrix} = \begin{bmatrix}
    0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\
    0 & 0 & 0 & 0 & 0 & -1 & 1 & -1 \\
    0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\
    0 & 0 & 0 & 0 & 0 & -1 & 1 & -1 \\
    0 & 1 & -1 & 0 & 1 & 0 & 0 & 0 \\
    -1 & -1 & 0 & -1 & -1 & 0 & 0 & 0 \\
    0 & 1 & 0 & 0 & 1 & 0 & 0 & 0 \\
\end{bmatrix} \begin{bmatrix}
    u_U \\
    u_{C1} \\
    u_{C2} \\
    u_{RL} \\
    u_{R1} \\
    i_{R2} \\
    i_L \\
    i_I \\
\end{bmatrix}
\]

Matrix 3: final matrix for the graph method shown in the Figure 6.

The above mentioned variables are,

- \(i_U\) in derivative form \(\left( \frac{d}{dt} u\right)\) = current through the voltage source,

- \(i_{C1}\) in derivative form \(\left( \frac{d}{dt} u_{C1}\right)\) = current through the capacitor C1,

- \(i_{C2}\) in derivative form \(\left( \frac{d}{dt} u_{C2}\right)\) = current through the capacitor C2,

- \(i_{RL}\) in derivative form \(\left( \frac{d}{dt} u_{RL}\right)\) = current through the resistor RL,

- \(i_{R1}\) in derivative form \(\left( \frac{d}{dt} u_{R1}\right)\) = current through the resistor R1,

- \(u_{R2}\) in derivative form \(\left( \frac{d}{dt} i_{R2}\right)\) = voltage through the resistor R2,
\( u_i \) in derivative form \( \left( \frac{d}{dt} i_i \right) \) = voltage through the inductor L,

\( u_i \) in derivative form \( \left( \frac{d}{dt} i_i \right) \) = voltage through the current source.

By solving the above shown matrix 3, we get the A, B, C and D matrix, which are shown below and for the detailed calculation see the *Appendix 1*.

And changing \( u \) into \( v \) for the input voltage \([v= -u]\).

\[
\begin{bmatrix}
\frac{d}{dt} u_{c1} \\
\frac{d}{dt} u_{c2} \\
\frac{d}{dt} i_L
\end{bmatrix}
= \begin{bmatrix}
-1/(R_1 + R_2) & 1/(R_1 + R_2) & 1 - R_1/(R_1 + R_2) \\
1/(R_1 + R_2) & -1/(R_1 + R_2) & R_1/(R_1 + R_2) \\
-1 + R_1/(R_1 + R_2) & -R_1/(R_1 + R_2) & -R_L - R_1R_1/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\
u_{c2} \\
i_L
\end{bmatrix}
+ \begin{bmatrix}
0 \\
0 \\
1
\end{bmatrix}
\begin{bmatrix}
y \\
v_i
\end{bmatrix}
\]

(11)

And the constitutive equation of the above mentioned matrix equation is,

\[
\begin{bmatrix}
\frac{d}{dt} u_{c1} \\
\frac{d}{dt} u_{c2} \\
\frac{d}{dt} i_i
\end{bmatrix}
= \begin{bmatrix}
-1/(R_1 + R_2)C_1 & 1/(R_1 + R_2)C_1 & 1/C_1 - R_1/(R_1 + R_2)C_1 \\
1/(R_1 + R_2)C_2 & -1/(R_1 + R_2)C_2 & R_1/(R_1 + R_2)C_2 \\
-1/L + R_1/(R_1 + R_2)L & -R_1/(R_1 + R_2)L & -R_L - R_1R_1/(R_1 + R_2)L
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\
u_{c2} \\
i_i
\end{bmatrix}
+ \begin{bmatrix}
0 \\
0 \\
1/L
\end{bmatrix}
\begin{bmatrix}
C_1 + R_1/(R_1 + R_2)C_1 \\
R_1/(R_1 + R_2)C_2 \\
R_1R_2/(R_1 + R_2)L
\end{bmatrix}
\]

(12)

And for finding the C and D matrices, from the below equation,

\[
\begin{bmatrix}
\frac{d}{dt} i_i
\end{bmatrix}
= \begin{bmatrix}
1 - R_1/(R_1 + R_2)R_1 & R_1/(R_1 + R_2)R_1 & R_1R_2/(R_1 + R_2)R_1
\end{bmatrix}
+ \begin{bmatrix}
0 \\
-R_1R_2/(R_1 + R_2)R_1
\end{bmatrix}
\]

(13)

These are the final A, B, C and D matrices according to the below structure.
Structure: \[
\begin{align*}
\dot{x} &= Ax + Bu \\
y &= Cx + Du
\end{align*}
\]

System Matrices:

\[
A = \begin{bmatrix}
-1/(R_1 + R_2)C_1 & 1/(R_1 + R_2)C_1 & 1/C_1 - R_1/(R_1 + R_2)C_1 \\
1/(R_1 + R_2)C_2 & -1/(R_1 + R_2)C_2 & R_1/(R_1 + R_2)C_2 \\
-1/L + R_1/(R_1 + R_2)L & -R_1/(R_1 + R_2)L & -R_L - R_1R_1/(R_1 + R_2)L
\end{bmatrix}
\]

(14)

\[
B = \begin{bmatrix}
0 & -1/C_1 + R_1/(R_1 + R_2)C_1 \\
0 & -R_1/(R_1 + R_2)C_2 \\
1/L & R_1R_2/(R_1 + R_2)L
\end{bmatrix}
\]

(15)

\[
C = \begin{bmatrix}
1 - R_1/(R_1 + R_2)R_1 & R_1/(R_1 + R_2)R_1 & R_1R_2/(R_1 + R_2)R_1
\end{bmatrix}
\]

(16)

\[
D = \begin{bmatrix}
0 & -R_1R_2/(R_1 + R_2)R_1
\end{bmatrix}
\]

(17)

3.2 Controllers used to control the Buck Converter:

The control methods are applied to maintain the stable output voltage from the buck converter. The controlling of buck converter can be accomplished by using different controllers. In our thesis we use the following controller’s Polynomial Controller, and Proportional Integral Derivative [PID] Controller.

These controller methods compare the actual output voltage with respect to the reference voltage. The difference between these voltages will lead the control element to attenuate the variations of the output voltage to the fixed reference voltage, this is known as voltage regulation.

3.3 Get average system:

The time averaged system can be derived as a weighting averaged of the two models. The averaged system ‘s’ average, system s_1 with voltage source and system s_2 without voltage...
source. Assuming the duty cycle with ‘d’, it can be seen that the output voltage of the converter varies linearly with the duty cycle for a given input voltage. As the duty cycle d is equal to the ratio between $t_{on}$ and the period T, it cannot be more than 1. Therefore this is why this converter is referred to as step-down converter, and then it has

$$s_{average} = ds_1 + (1-d)s_2$$

(18)

As it can be observed that $ABCD$ matrix of the two systems, that $ACD$ matrices are the same while the $B$ matrix are different in the ON and OFF states.

So only need to take the average value of $B$ matrix.

Where $B_{11} = \begin{bmatrix} 0 & -1/C_1 + R_1/(R_1 + R_2)C_1 \\ 0 & -R_1/(R_1 + R_2)C_2 \\ 1/L & R_1R_2/(R_1 + R_2)L \end{bmatrix}$ and $B_{21} = \begin{bmatrix} 0 & -1 + R_1/(R_1 + R_2) \\ 0 & -R_1/(R_1 + R_2) \\ 0 & R_1R_2/(R_1 + R_2) \end{bmatrix}$

Average system equation,  \( B = d(B_{11} + B_{12}) + (1-d)(B_{21} + B_{22}) \)

(19)

Now substituting the below $B_{11}, B_{12}, B_{21}$ and $B_{22}$ matrices in above equation 19, they are

$$B_{11} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ 1/L & 0 \end{bmatrix}, \quad B_{12} = \begin{bmatrix} 0 & -1/C_1 + R_1/(R_1 + R_2)C_1 \\ 0 & -R_1/(R_1 + R_2)C_2 \\ 0 & R_1R_2/(R_1 + R_2)L \end{bmatrix}, \quad B_{21} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ 0 & 0 \end{bmatrix}, \quad B_{22} = \begin{bmatrix} 0 & -1 + R_1/(R_1 + R_2) \\ 0 & -R_1/(R_1 + R_2) \\ 0 & R_1R_2/(R_1 + R_2) \end{bmatrix}$$

Substituting the above $B_{11}, B_{12}, B_{21}$ and $B_{22}$, Matrix’s in the above equation 19, then

$$B = d \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ 1/L & 0 \end{bmatrix} + \begin{bmatrix} 0 & -1/C_1 + R_1/(R_1 + R_2)C_1 \\ 0 & -R_1/(R_1 + R_2)C_2 \\ 0 & R_1R_2/(R_1 + R_2)L \end{bmatrix} \begin{bmatrix} V_S \\ I_0 \end{bmatrix} +$$
After multiplying the above matrix with ‘d’ and [1-d], then simplifying the above equation, by taking ‘d’ adjacent matrix as common,

$$= \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ V_5 / L & 0 \end{bmatrix} \begin{bmatrix} 0 - I_0 / C_1 + R_1 / (R_1 + R_2) C_1 \\ 0 - R_1 I_0 / (R_1 + R_2) C_2 \\ R_1 R_2 I_0 / (R_1 + R_2) L \end{bmatrix} + (1-d) \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ V_5 / L & 0 \end{bmatrix} \begin{bmatrix} 0 - I_0 / C_1 + R_1 / (R_1 + R_2) C_1 \\ 0 - R_1 I_0 / (R_1 + R_2) C_2 \\ R_1 R_2 I_0 / (R_1 + R_2) L \end{bmatrix}$$

The averaged system is:

$$\dot{x} = Ax + Bu$$

$$y = Cx + Du$$

And the continuous-time system is,

$$\begin{bmatrix} \frac{d}{dt} u_{c1} \\ \frac{d}{dt} u_{c2} \\ \frac{d}{dt} i_L \end{bmatrix} = \begin{bmatrix} 0 - 1 / C_1 + R_1 / (R_1 + R_2) C_1 \\ 0 - R_1 I_0 / (R_1 + R_2) C_2 \\ -1 / L \end{bmatrix} \begin{bmatrix} u_{c1} \\ u_{c2} \\ i_L \end{bmatrix} + \begin{bmatrix} 0 - I_0 / C_1 + R_1 / (R_1 + R_2) C_1 \\ 0 - R_1 I_0 / (R_1 + R_2) C_2 \\ R_1 R_2 I_0 / (R_1 + R_2) L \end{bmatrix} \begin{bmatrix} d \\ V_5 / L \end{bmatrix}$$

Since $B$ is the average result of the switched system were the input voltage, capacitor, resistor and inductor values are fixed. Here the $B$ value depends on the duty cycle $d$ and $i_0$. So the system we get is linear with the duty cycle [control signal] $d$ and $i_0$. 

(20)
Chapter 4. Polynomial Pole Placement Controller:

This section explains designing of a Polynomial Controller with Pole Placement for the third order buck plant. We used discrete time case because, the implementation should be done in a computer.

To develop controller structure form the pole placement controller, which gives the closed system of exact poles defined by own dynamics characteristics [7]. This does not include any direct comparison between the set-point and actual value according to rules of error. But it exposes the set-point value and actual value for the more individual treatment doing so, is $C(z)$ and $D(z)$ polynomials with the delay operator $z^{-1}$ [7]. As our buck converter plant is of third order we have consider five parameters, from the design and output of the second order polynomial pole placement controller [5] i.e., $c_1, c_2, d_0, d_1$ and $d_2$ to achieve necessary design freedom.

\begin{align*}
C(z) &= 1 + c_1 z^{-1} + c_2 z^{-2} \\
D(z) &= d_0 + d_1 z^{-1} + d_2 z^{-2}
\end{align*}

\begin{equation}
(22) \quad (23)
\end{equation}

The determined equation depicting from the above figure 9 is

\[ C(z) \cdot U(z) = K_r \cdot Y_{ref}(z) - D(z) \cdot Y(z) \]

Which by above equations 22 and 23, the interpretation of $z^{-1}$ the delay operator gives the differential equation
The controller relationship in the equation (24), defines the control signal at the \( k \)th sampling time as a function of the set-point and output measurement.

The closed circuit transfer function in figure 9, is given by,

\[
\frac{Y(z)}{Y_{ref}(z)} = K_r \cdot \frac{H(z)}{C(z) + H(z) \cdot D(z)}
\]  

(25)

We know that \( H(z) = \frac{B(z)}{A(z)} \)

The \( H(Z) \) is a discretized time continuous transfer function, and the discretization is performed with zero order hold circuit with the help of Matlab c2d function.

Substituting the \( H(z) \) in equation (25), we get as shown below,

\[
\frac{Y(z)}{Y_{ref}(z)} = K_r \cdot \frac{B(z)}{A(z)C(z) + B(z) \cdot D(z)}
\]  

(26)

The design comprises of the \( C \) and \( D \) polynomials.

The characteristic polynomial is named as \( P(z) \).

Now \( P(z) = A(z)C(z) + B(z) \cdot D(z) \)

Re-writing the equation (26), as shown below

\[
\frac{Y(z)}{Y_{ref}(z)} = K_r \cdot \frac{B(z)}{P(z)}
\]  

(27)
The complete analytical designing procedure is as follows.

Choose the plant of order. Here we have $3^{rd}$ order Buck converter plant.

$$H(z) = \frac{B(z)}{A(z)} = \frac{b2z^2 + b2z + b3}{z^3 + a1z^2 + a2z + a3} = \frac{b1z^{-1} + b2z^{-2} + z^{-3}}{1 + a1z^{-1} + a2z^{-2} + a3z^{-3}}$$

The characteristic polynomial equation is $P(z) = A(z)C(z) + B(z) \cdot D(z)$  \hspace{1cm} (28)

Substitute $C(z)$ and $D(z)$ values in above equation 28, then

$$C(z^{-1}) = (1 + c_1 z^{-1} + c_2 z^{-2})$$

$$D(z^{-1}) = (d_0 + d_1 z^{-1} + d_2 z^{-2})$$

$$P(z) = (1 + a1z^{-1} + a2z^{-2} + a3z^{-3})(1 + c_1 z^{-1} + c_2 z^{-2}) + (b1z^{-1} + b2z^{-2} + z^{-3})(d_0 + d_1 z^{-1} + d_2 z^{-2})$$

\hspace{1cm} (29)

Multiplying the above equation on RHS [Right Hand Side] gives

$$\begin{align*}
A(z)C(z) + B(z) \cdot D(z) &= 1 + c_1 z^{-1} + c_2 z^{-2} + a1c_1 z^{-3} + a1c_2 z^{-4} + a2c_1 z^{-3} + a2c_2 z^{-4} + a3z^{-3} + a3c_1 z^{-4} + a3c_2 z^{-5} + b1d_0 z^{-1} + b1d_1 z^{-2} + b1d_2 z^{-3} + b2d_0 z^{-2} + b2d_1 z^{-3} + b3d_0 z^{-3} + b3d_1 z^{-4} + b3d_2 z^{-5} \\
\end{align*}$$

\hspace{1cm} (30)

Take $z^{-1}, z^{-2}, z^{-3}, z^{-4}, z^{-5}$ as common in above equation 29. The resultant equation is

$$1 + (c_1 + a1 + b1d_0) z^{-1} + (c_2 + a2 + b1d_1 + b2d_0) z^{-2} + (a1c_2 + a2c_1 + a3 + b1d_2 + b2d_1 + b3d_0) z^{-3} + (a2c_2 + a3c_1 + b2d_2 + b3d_1) z^{-4} + (a3c_2 + b3d_2) z^{-5}$$

\hspace{1cm} (31)
By choosing the controller parameters, we can freely design the closed loop system pole placement.

Finding poles $P_1$, $P_2$, $P_3$, $P_4$ and $P_5$ in terms of $q_1, q_2, q_3, q_4$ and $q_5$

$$P(z) = (1 - q_1 z^{-1})(1 - q_2 z^{-1})(1 - q_3 z^{-1})(1 - q_4 z^{-1})(1 - q_5 z^{-1}) = 1 + p_1 z^{-1} + p_2 z^{-2} + p_3 z^{-3} + p_4 z^{-4} + p_5 z^{-5} \quad (32)$$

Finding the values $P_1, P_2, P_3, P_4, \text{ and } P_5$.

- $P_1 = -q_5 - q_4 - q_3 - q_2 - q_1$
- $P_2 = q_4 q_5 + q_3 q_5 + q_3 q_4 + q_2 q_5 + q_2 q_4 + q_2 q_3 + q_1 q_5 + q_1 q_4 + q_1 q_3 + q_1 q_2$
- $P_3 = -q_3 q_4 q_5 - q_2 q_4 q_5 - q_2 q_3 q_5 - q_2 q_3 q_4 - q_1 q_4 q_5 - q_1 q_3 q_5 - q_1 q_3 q_4 - q_1 q_2 q_5 - q_1 q_2 q_4 - q_1 q_2 q_3$
- $P_4 = q_2 q_3 q_4 q_5 + q_1 q_3 q_4 q_5 + q_1 q_2 q_4 q_5 + q_1 q_2 q_3 q_5 + q_1 q_2 q_3 q_4$
- $P_5 = -q_1 q_2 q_3 q_4 q_5$

For the remaining derivation please go to the Appendix 2.

Finally we get the $F$, $M$ and $N$ matrices.

Form the simplified equation in Appendix-2, let the matrices be,

$$M = \begin{pmatrix} 1 & 0 & b_1 & 0 & 0 \\ a_2 & a_1 & b_3 & b_2 & b_1 \\ a_3 & a_2 & 0 & b_3 & b_2 \\ 0 & a_3 & 0 & 0 & b_3 \end{pmatrix}, \quad F = \begin{pmatrix} c_1 \\ c_2 \\ d_0 \\ d_1 \\ d_2 \end{pmatrix}$$
The polynomials $c_1, c_2, d_0, d_1$ and $d_2$ are obtained by $F = M^{-1} \times N$

The analytical calculation of 5x5 matrix is pretty tough, so we used Matlab for a numerical solution. The values for poles q1, q2, q3, q4 and q5 are considered to be 0.5, 0.5, 0.5, 0.5 and 0.5 respectively.

Here we had taken 0.5 value for poles because the output is not stable for the remaining combinations mentioned below for both the PID, PD and Polynomial controllers. And the resulted graphs for Polynomial Pole Placement are provided in the Appendix 3.

At $2 \times 10^{-6}$, the following pole combination are consider

$q1 = 0.1, q2 = 0.2, q3 = 0.3, q4 = 0.4, q5 = 0.5.$

$q1 = 0.3, q2 = 0.4, q3 = 0.5, q4 = 0.6, q5 = 0.7$ and

$q1 = 0.5, q2 = 0.5, q3 = 0.5, q4 = 0.5, q5 = 0.5$

At $2 \times 10^{-6}$, and poles $q1 = 0.5, q2 = 0.5, q3 = 0.5, q4 = 0.5, q5 = 0.5$, the result is better than the remaining combinations, so we consider these pole values for the PID, PD and Polynomial controllers.

And the below specified formula [8], for finding multiple poles resulted approximately same as above mentioned iterations. Here we have to find the sampling interval ‘h’ from the below formula, by substituting the bandwidth ‘$\omega_b$’ and number of samples ‘N’, the N value varies from $10 - 20$ here we had considered $N = 20$ and bandwidth $\omega_b = 9k$, because to get the output value less than $5\mu s$ we can check the scanner- pole placement for a multiple poles table 16.1 from [8], shown below
Table 1: Pole Placement for Multiple poles.

<table>
<thead>
<tr>
<th>m</th>
<th>Z=p</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.73</td>
</tr>
<tr>
<td>2</td>
<td>0.61</td>
</tr>
<tr>
<td>3</td>
<td>0.54</td>
</tr>
<tr>
<td>4</td>
<td>0.49</td>
</tr>
<tr>
<td>5</td>
<td>0.44</td>
</tr>
</tbody>
</table>

\[
h = \left( \frac{2\pi}{N\omega_b} \right)
\]  

(36)

Were \( h \) = sampling interval, 

\( N = 20 \), and 

\( \omega_b \) = Bandwidth is 9k.

And the sampling interval \( h \) will be,

\[
h = \left( \frac{2\pi}{20 \times 2 \times 9k} \right)
\]

\( h = 4e^{-6} \)

So from the above formula we get the sampling interval as \( 4e^{-6} \), which is used in the Polynomial pole placement Matlab code [Appendix 8], as sampling interval for the controller to obtain the reference voltage 3.3v.

4.1 Polynomial Controller Simulation:

After obtaining the mathematical equations for Polynomial Controller, the next task is to build the system in Simulink and program the procedure in Matlab. The controller and plant is to be built same as the structure shown in the Figure 9. The polynomial control structure, as given in Figure 9, can be implemented in Simulink as presented in Figure 10.
4.2 Building the Controller block in Simulink:

Here considering the output voltage as the input of controller and comparing it by the reference voltage as shown in the below Figure 10.

![Figure 10: Polynomial Controller block](image)

The block diagram Figure 10, depicts the discrete polynomial design which consists of 3 discrete filters such as \( \text{cpoly} \), \( \text{dpoly} \) and the plant \( \text{Bdtf(z)}/\text{Adtf(z)} \) were the \( \text{cpoly}(z) \) and \( \text{dpoly}(z) \) polynomial equations are

\[
C(z) = 1 + c_1 z^{-1} + c_2 z^{-2}, \quad D(z) = d_0 + d_1 z^{-1} + d_2 z^{-2}.
\]

4.3 Results of Polynomial Controller:

These block diagrams are with the help of appropriate Matlab files to obtain the below resulted graphs.
4.3.1 Simulation results of the Continuous and Discrete Model:

The simulation is an numerical illustration of the theory. And while designing the controller, the saturation limits should not be reached too much for a reasonable disturbance, and the simulation result satisfies it.

From the Figure 11, we can observe that, the output voltage of discrete system satisfies the required voltage and the output of the controller gets stable quickly [in 18 sample intervals] and remains stable. The desired output voltage [reference voltage 3.3V] by the simulations derived controller with a time continuous model.

The controller is also tested controlling the time continuous system, the average system model consists of the polynomial controller and a continuous system. For Simulation result, see Figure 11.
Since the reference voltage is 3.3V, the output voltage can reach it very well. We can observe that the output voltage remains stable at 3.3V. The result shows that the continuous model of Polynomial Controller works perfectly. The controller attains the output voltage to be stable quickly [in 50 μs] and remain the desired output voltage [reference voltage 3.3V]. It is very nearer to the polynomial controller theory. Figure 12.
4.3.2 Simulation results of Switched system without using Disturbance and Noise:

The model is a switched time continuous model, which is an interesting part of our work. It investigates how good the controller works for this system with disturbance.

![Simulation result](image)

Figure 14: Simulation result, without disturbance, and noise.

It can be observe from Figure 14 that the output voltage is quite stable near 3.38V which is very nearer to the given reference voltage i.e., 3.38v.

As it can be observed from figure 14, that the output voltage of discrete system can satisfy the required voltage. And the controller output is also very stable near 0.2816 = 3.38/12 [reference voltage divide input voltage], and remain the wanted output voltage [reference voltage 3.3V]. The simulation is according to the polynomial controller theory, so it is an illustration of the theory by numerical calculations.

When designing the controller, it should not hit the saturation limits too much for a reasonable disturbance. And the simulation result satisfies it.
Here Figure 14 is the resulted graph of the block diagram shown below Figure 15.

Figure 15: PWM Model with Controller

4.3.3 Simulation result without Disturbance and with Noise:

In the realistic case, the circuit has noise, at the switch with respect to high frequency switching measurement noise and so on.
Here, adding some band-limited white noise to simulate it, and the noise magnitude is between -0.2 and 0.2, and the noise frequency is 33MHz.
The frequency is a very rough estimation based on a measurement, see Fig. 16. And the measurements are introduced by the switching, but use it the whole time
The noise applied in this simulation is satisfactory. Thus the same amount of noise is considered in the simulation. This noise is summed up with the output voltage as the measurement noise. And the output voltage with noise is shown in Figure 17.
It can be observable that after adding some amount of noise, the output is still stable. But the noise is not huge, it has some effect on the stability of controller. In this range 3.0V-3.5V the output does not get stable. The result shows that the controller with noise is less stable when compared to the controller without noise.

For the noise position block diagram, see figure 15.

### 4.3.4 Simulation result with Disturbance and without Noise:

In this model, the current source is applied as the load, when the output current abruptly changes, but there is some sort of disturbance in the output voltage.

To notice, the disturbance and check whether the Polynomial Controller has ability to turn back the output voltage value equal to reference voltage value.
The output voltage under control is about 3.309 V it is quite close to the reference voltage 3.3V; it means that the controller is in good state.

At the 60th sampling interval [180μs] changing the current at the load from 5A to 15A, the output voltage falls from 3.309V to 2.883V but it recover to the stable voltage 3.355V shortly [in 10 sample intervals].

The drop percentage is \( \frac{3.309-2.883}{3.309} = 12\% \), which is adequate for the good performance. When the load current changes abruptly, the controller controls the output voltage and recovers to the stable voltage soon.
4.3.5 Simulation result with disturbance and with noise:

Adding the same noise like what it is done in above section 4.3.4 for simulation result, see Figure 19.

![Figure 19: Simulation result, with Disturbance and Noise](image)

As it can be observable, after summing the noise, the output voltage is stable. And the output voltage is closer to the reference voltage were the controller output is close to 0.280.

4.3.6 Simulation results with high value resistors:

If we consider all the three resistors $R_L, R_1$ and $R_2$ with large values [100e$^{-3}$], with the Polynomial Controller, after running the simulation we can observe the controller quality from the below graph in Figure 20. We have also simulated with slight change in capacitors and inductors. We have observed that there is no noticeable change in results.
As it can be seen from above figure 20, with large resistors results in bad simulation result. The output voltage did not reach the stable reference voltage 3.3V, and it can be observed by viewing the control signal $d$, that the system is not stable.

By modifying the controller design the system could be stable. The modified design implies that the closed system gets a lower bandwidth.
Chapter 5. PID Controller:

Here the PID stands for P -Proportional, I - Integral, D - Derivative. A mathematical function described by PID is applied to the error signal.

\[ V_{error} = V_{set} - V_{sensor} \] (37)

Here \( V_{sensor} \) defines where you are and \( V_{set} \) represents the difference between where you want to go. Very simple, but yet so powerful, if controller is tuned correctly the signal \( V_{sensor} \) will move closer to \( V_{set} \).

Tuning a system means adjusting three variables \( K_p, K_i \) and \( K_d \) adding in various amounts of these functions to get the system to behave the way you want too.

5.1 Transfer Function of PID:

Consider the continuous-time PID Controller transfer function [9] as shown below,

\[ H_{PID}(s) = K \left[ 1 + \frac{1}{T_i s} + \frac{T_d s}{1 + \frac{T_d}{N} s} \right] \] (38)

This controller is designed by four tuning parameters:

- \( K \) – proportional gain
- \( Ti \) – integral action
- \( Td \) – derivative action
- \( Td /N \) – filtering of the derivative action

Various discretization methods may be applied to design the structure of the digital PID Controller. The relation among the parameters of the continuous-time and discrete-time will completely depend on the method used, but the structure remains the same for the digital PID Controller [8].

In our case the controller’s design and implementation is in the discrete-time, the method of discretization is not crucial. We used the backward difference approximation. It follows that derivative \( s \) will be approximated by \( (1 - z^{-1}) / T_z \) and \( 1 / s \) [integration] will be approximated by \( T_z / (1 - z^{-1}) \) [9]. As it is a real time system the possible way is to approximate it with some kind of backward approximation. So, we used backward difference approximation for the system.
This produces

\[ \frac{1}{T_s} = \frac{T_s}{T_i} \cdot \frac{1}{1 - z^{-1}} \]

\[ T_d s = \frac{T_d}{T_s} \cdot (1 - z^{-1}) \]

\[ \frac{1}{1 + \frac{T_d}{N} s} = \frac{1}{1 + \frac{T_d}{N T_s} (1 - z^{-1})} = \frac{\frac{N T_s}{T_d + N T_s}}{1 - \frac{T_d}{T_d + N T_s} z^{-1}} \]

(39)

By introducing the above expressions in Equation 38, the pulse transfer function [Operator] of the digital PID Controller is obtained:

\[ H_{PID}(z^{-1}) = \frac{R(z^{-1})}{S(z^{-1})} = K \left[ 1 + \frac{T_s}{T_i} \cdot \frac{1}{1 - z^{-1}} + \frac{\frac{N T_s}{T_d + N T_s} \cdot (1 - z^{-1})}{1 - \frac{T_d}{T_d + N T_s} z^{-1}} \right] \]

(40)

Here \( R(z^{-1}) \) and \( S(z^{-1}) \) are same similar as that of C and D notation of the parameters in polynomial controller.

The ratio of two polynomials expression is obtained by adding the three terms. Polynomials \( R \) and \( S \) have the following form,

\[ R(z^{-1}) = (r0 + r1 z^{-1} + r2 z^{-2}) \]

\[ S(z^{-1}) = (1 + s1 z^{-1} + s2 z^{-2}) \]

Finding parameters \( r0, r1, r2, s1 \) and \( s2 \).

For simplicity consider \( G = \frac{T_d}{T_d + N T_s} \) and substitute in equation 40
Then equation becomes

\[
K \left[ 1 + \frac{T_s}{T_i} \left( \frac{1}{1 - z^{-1}} \right) + \frac{GNT_s}{T_d} \left( \frac{1}{1 - G \cdot z^{-1}} \right) \right]
\]

\[
= \frac{\left( 1 - G \cdot z^{-1} \right) \left( T_i - T_i \cdot z^{-1} \right) + \frac{GT_i}{T_d} \left( 1 - z^{-1} \right) + T_s \left( 1 - G \cdot z^{-1} \right)}{T_i \left( 1 - z^{-1} \right) \left( 1 - G \cdot z^{-1} \right)}
\]

Now consider Denominator Part \( T_i \left( 1 - z^{-1} \right) \left( 1 - G \cdot z^{-1} \right) \)

Now Substitute \( G = \frac{T_d}{T_d + NT_s} \)

\[
T_i \left( 1 - z^{-1} \right) \left( 1 - G \cdot z^{-1} \right) = T_i \left( 1 - z^{-1} \right) \left( 1 - \frac{T_d}{T_d + NT_s} \cdot z^{-1} \right)
\]

\[
= T_i \left[ 1 - \frac{T_d}{T_d + NT_s} \cdot z^{-1} - z^{-1} \frac{T_d}{T_d + NT_s} \cdot z^{-1} \right]
\]

\[
= T_i \left[ 1 - z^{-1} \left( -\frac{2T_d + NT_s}{T_d + NT_s} \right) + \frac{T_d}{T_d + NT_s} \right] z^{-2}
\]

\[
= 1 - z^{-1} \left[ -\frac{2T_d + NT_s}{T_d + NT_s} \right] + \frac{T_d}{T_d + NT_s} z^{-2}
\]

\[
= 1 + z^{-1} \left[ -\frac{2T_d + NT_s}{T_d + NT_s} \right] + \frac{T_d}{T_d + NT_s} z^{-2} = 1 + s_1 z^{-1} + s_2 z^{-2} = \left( 1 - z^{-1} \right) \left( 1 + s'_1 z^{-1} \right)
\]

Now \( s'_1 = -s_2 = -\frac{T_d}{T_d + NT_s} \) and \( s_1 = \left[ -\frac{2T_d + NT_s}{T_d + NT_s} \right] \)
Now for simplicity again take \( G = \frac{T_d}{T_d + NT_s} \)

Now take numerator part from equation 42 i.e.

\[
K \left[ (1 - G \cdot z^{-1}) (T_i - T_i \cdot z^{-1}) + \frac{G T_i (1 - z^{-1}) T_i (1 - z^{-1}) + T_s (1 - G \cdot z^{-1})}{T_d} \right]
\]

\[
K \left[ T_i - T_i z^{-1} - G T_i z^{-1} + G T_i z^{-2} + T_s - G T_s z^{-1} + \frac{G N T_s T_i}{T_d} - \frac{2 G N T_s T_i z^{-1}}{T_d} - \frac{G N T_s T_i z^{-2}}{T_d} \right]
\]

Now taking \( z^{-1} \) and \( z^{-2} \) as common in above equation

\[
K \left[ z^{-1} \left[ -T_i - G T_i - \frac{G T_i}{T_d} - \frac{2 G N T_s}{T_d} \right] + z^{-2} \left[ G - \frac{G N T_s}{T_d} \right] + \left[ T_i + T_s + \frac{G N T_s}{T_d} \right] \right]
\]

Now taking \( T_i \) as common in above equation

\[
T_i K \left[ z^{-1} \left[ -1 - G - \frac{G T_i}{T_d} - \frac{2 G N T_s}{T_d} \right] + z^{-2} \left[ G - \frac{G N T_s}{T_d} \right] + \left[ 1 + \frac{T_s}{T_i} + \frac{G N T_s}{T_d} \right] \right]
\]

Now Substitute \( G = \frac{T_d}{T_d + NT_s} \)

\[
K \left[ 1 + \frac{T_s}{T_i} + \frac{T_d}{T_d + NT_s} \cdot \frac{NT_s}{T_d} \right] + K z^{-1} \left[ -1 - \frac{T_d}{T_d + NT_s} - \frac{T_d}{T_d + NT_s} \cdot \frac{T_s}{T_i} - 2 \frac{T_d}{T_d + NT_s} \cdot \frac{NT_s}{T_d} \right] +
\]

\[
K z^{-2} \left[ \frac{T_d}{T_d + NT_s} - \frac{T_d}{T_d + NT_s} \cdot \frac{NT_s}{T_d} \right] = r_0 + r_1 z^{-1} + r_2 z^{-2}
\]

We know that \( s_i' = -\frac{T_d}{T_d + NT_s} \) and substitute in 43 equation.
\[
K \left[ 1 + \frac{T_s}{T_i} - s_1' \cdot \frac{NT_s}{T_d} \right] + z^{-1} \left[ -1 + s_1' + \frac{T_s}{T_i} - s_1' + 2s_1' \cdot \frac{NT_s}{T_d} \right] = r_0 + r_1z^{-1} + r_2z^{-2}
\]  
(47)

From equation 44 and 47 we get

\[
r_0 = K \left[ 1 + \frac{T_s}{T_i} + \frac{T_d}{T_d + NT_s} \cdot \frac{NT_s}{T_d} \right]
\]  
(48)

\[
r_1 = K \left[ -1 - \frac{T_d}{T_d + NT_s} \left[ 1 + \frac{T_s}{T_i} + 2 \cdot \frac{NT_s}{T_d} \right] \right]
\]  
(49)

\[
r_2 = K \left[ -s_1' + s_1' \cdot \frac{NT_s}{T_d} \right] = K \frac{T_d}{T_d + NT_s} \left[ 1 + \frac{NT_s}{T_d} \right]
\]  
(50)

\[
s_1 = \frac{2T_d + NT_s}{T_d + NT_s}
\]  
(51)

\[
s_2 = \frac{T_d}{T_d + NT_s}
\]  
(52)

From the parameters \(r_0, r_1, r_2, s_1\) and \(s_2\) we get \(K, T_i, T_d\) and \(\frac{T_d}{N}\).

\[
K = \frac{r_0 \left[ - \frac{T_d}{T_d + NT_s} \right] - r_1 - (2 - \frac{T_d}{T_d + NT_s})r_2}{(1 + -\frac{T_d}{T_d + NT_s})^2}
\]  
(53)

\[
T_i = T_s \cdot \frac{K(1 - \frac{T_d}{T_d + NT_s})}{r_0 + r_1 + r_2}
\]  
(54)
\[ T_d = T_s \left[ - \frac{T_d}{T_d + NT_s} \right] r_0 + \frac{T_d}{T_d + NT_s} + r_2 \]

\[ K \left[ 1 - \frac{T_d}{T_d + NT_s} \right]^3 \]

\[ T_d = \frac{T_s}{N} \left[ - \frac{T_d}{T_d + NT_s} \right] T_s \]

\[ 1 - \frac{T_d}{T_d + NT_s} \]

5.2 Structure of the Digital PID Controller:

The digital PID Controller has five parameters \([r0, r1, r2, s1, s2]\) as the continuous time PID Controller.

The pulse transfer function [operator] of the digital PID Controller contains as a common factor of the denominator the term \((1 - \frac{1}{N})\), it assures the behavior of the numerical integration. The denominator also contains the term \((1 - s1)z^{-1}\), this is a digital filter which plays the role of the filter \([1 + (T_d / N) s]\) in the continuous-time PID Controller.

Figure 21: Equivalent block diagrams of a digital PID Controller.

Figure 22: Equivalent block diagrams of a digital control loop using the digital PID Controller.
Computation of the coefficients of the Digital Controller:

\[
H(z) = \frac{B(z)}{A(z)} = \frac{b_1 z^{-2} + b_2 z^{-1} + b_3}{z^3 + a_1 z^{-2} + a_2 z^{-1} + a_3}
\]  

(57)

\[
P(z^{-1}) = A(z^{-1})S(z^{-1}) + B(z^{-1})R(z^{-1})
\]

\[
R(z^{-1}) = (r_0 + r_1 z^{-1} + r_2 z^{-2})
\]

\[
S(z^{-1}) = (1 + s_1 z^{-1} + s_2 z^{-2})
\]

\[
P(z^{-1}) = A(z^{-1})S(z^{-1}) + B(z^{-1})R(z^{-1})
\]

\[
= (1 + a_1 z^{-1} + a_2 z^{-2} + a_3 z^{-3})(1 + s_1 z^{-1} + s_2 z^{-2}) + (b_1 z^{-1} + b_2 z^{-2} + z^{-3})(r_0 + r_1 z^{-1} + r_2 z^{-2})
\]

(58)

The remaining equation is in Appendix 4.

The values for poles \(q_1, q_2, q_3, q_4\) and \(q_5\) have been chosen as 0.5, 0.5, 0.5, 0.5 and 0.5 respectively.

Here we had taken 0.5 for the poles because, we had tried different combinations of the poles with frequency for both the PID and Polynomial controllers i.e.

The resulted graphs are seen in the Appendix-5.

At \(10^{-6}\), the following pole combination are consider

\(q_1 = 0.1, q_2 = 0.2, q_3 = 0.3, q_4 = 0.4, q_5 = 0.5\).

\(q_1 = 0.3, q_2 = 0.4, q_3 = 0.5, q_4 = 0.6, q_5 = 0.7\) and

\(q_1 = 0.5, q_2 = 0.5, q_3 = 0.5, q_4 = 0.5, q_5 = 0.5\)

At this output \(10^{-6}\), and at poles \(q_1 = 0.5, q_2 = 0.5, q_3 = 0.5, q_4 = 0.5, q_5 = 0.5\), the result is better than the remaining combinations, so we consider these pole values for both the controllers. And the same formula is used as in equation 26.
5.3 Simulation results of the Continuous and Discrete model:

The below graph Figure 23, is the output of the discrete model

![Discrete model output graph](image)

We can observe that, the output voltage of discrete system is little different to the required voltage. Were the reference voltage is 3.3 but the discrete model output is 2.904, which is 0.3 volts different from reference voltage.

The output of the controller gets stable quickly [in 20 sample intervals] and remains stable. The controller attains the output voltage come to stable status quickly [in 20 sample intervals] and remains the output voltage 2.9V [reference voltage 3.3V].

The simulation is based on Digital PID Controller theory; it is a representation of the theory by numerical calculations. While designing the controller, the saturation limits should not be reached too much for a reasonable disturbance. And the simulation result satisfies it.

The Discrete time Simulink model without saturation for PID Controller, shown below.
Since the reference voltage is 3.3V, the output voltage did not reach it very well. We can observe that the output voltage remains stable at nearly at 3V.
The controller attains the output voltage to be stable quickly [in 50 μs] and remains stable below the reference voltage [3.3V].

The Continuous time Simulink model without saturation for PID Controller, shown below.

![Continuous Time Model without Saturation](image)

**Figure 26: Continuous Time Model without Saturation.**

### 5.4 Simulation result without disturbance and without noise:
The model is a switched time continuous model. From these results we can tell that how good the controller works for this system with disturbance.

![Simulation output for without disturbance and without noise](image)

**Figure 27: Simulation output for without disturbance and without noise.**
It can be observable from the above output figure, the output voltage [3v] is quite stable and not so nearer to the given reference voltage i.e. 3.3v. And the controller output is also very stable near \(0.182 = \frac{3.023}{12}\) [reference voltage divide input voltage].

The block diagram of PWM model with controller Simulink is shown below.

![Figure 28: PWM model with controller Simulink](image)

**5.5 Simulation result without Disturbance and with Noise:**

In the realistic case, the circuit has little noise, from the switch with respect to high frequency switching, measurement noise and so on. Here, we add some band-limited white noise in simulation.

The noise frequency is 33MHz and the noise magnitude is between -0.2 and 0.2, here frequency is a approximate estimation based on measurement; see Figure 30 and the measurements are introduced by the switching, but use it the whole time.
Figure 29: Simulation output for without disturbance with noise

The noise applied in this simulation is very suitable, and it is moderate i.e., nor small or big. So here we used the same form of noise in the simulation.

The noise is sum up with the output voltage as the measurement noise and the output voltage with noise can be seen in Figure 30.
It can be observable after adding some amount of noise, the output is not stable, but the noise is not huge it has effect on the stability of controller. When the output couldn’t get stable [range is 3.0V-3.5V] as the result, the controller with noise is less stable when compared to the controller without noise.

5.6 Simulation result with Disturbance and without Noise:

In this model, the current source is applied as the load, when the output current abruptly changes; there is some sort of disturbance in the output voltage.

To notice, the disturbance and check whether the PID Controller has ability to turn back the output voltage to reference voltage. Set the current change range as 5A-15A see in Figure 30.

![Figure 30: simulation output for voltage measurement with noise and without disturbance.](image)
The output voltage under control is about 3.018 V; it is not quite close to the reference voltage 3.3V; it means the controller is not good enough. Then changing the current at the load from 5A to 15A, the output voltage falls from 3.018V to 2.617V.

It recover to the stable voltage 3.001V shortly [in 10 sample intervals] when the load current changes abruptly, the controller output voltage recovers to the stable voltage soon.

**5.7 Simulation result with Disturbance and Noise:**

Adding the same noise like, what was done in section 5.6 and for the simulation results we can refer to Figure 32.
As it can be observable, after summing the noise, the output voltage is not as stable as before. The output voltage is not closer to the reference voltage and the controller output is close to 0.280 but not so stable.

The simulation need to try large resistors, and observe the controller quality. For the simulation result, see Figure 33.
As it can be observable, after summing the noise, the figure 33 shows the output voltage with oscillations. When compared with the polynomial controller the PID did not reached the given reference voltage 3.3v. While the polynomial controller performed well with the reference voltage.
Chapter 6. PD Controller:

In this Controller describes three basic mathematical functions applied to the error signal, 
\[ V_{error} = V_{set} - V_{sensor} \] here the controller performs the PD mathematical functions on the error and applies their sum to the system.

Proportional-Derivative control is useful for fast response controllers, here we had discussed about the two controller actions.

**Proportional Action:**

Proportional action gives a quick response to the control error. This is useful for improving the response of a stable system. Additionally, the gain is the same for all frequencies leaving the system with a none zero steady-state error.

**Derivative Action:**

Derivative action acts on the rate of change of the control error. This provides a fast response, as opposed to the integral action, it will produce large control signals in response to high frequency control errors such as set point changes [step command] and measurement noise.

6.1 Structure of the digital PD controller:

The digital PD Controller has five parameters \([d0, d1, & c1] \) as the continuous time PD Controller.

The pulse transfer function [operator] of the digital PID Controller contains as a common factor of the denominator the term \((1 - z^{-1})\), it assures the behavior of the numerical integration. The denominator also contains the term \((1 - s1z^{-1})\), this is a digital filter which plays the role of the filter \([1 + (Td/N) s]\) in the continuous-time PD Controller.
Here in this thesis we considering a second order PD Controller plant, with the transfer function obtained from the plant

\[
\frac{k(1+T_d s)}{1 + \frac{T_d}{N} s}
\]  

(66)

And discredited according to Tustin method.
Substituting $s$ value in equation 66,
\[
\frac{1 + T_d \frac{2}{h} 1 - z^{-1}}{K} \cdot \frac{1 + \frac{T_d}{N} \frac{2}{h} 1 - z^{-1}}{1 + \frac{T_d}{N} \frac{2}{h} (1 - z^{-1})}
\]

\[
\frac{1 + z^{-1} + T_d \frac{2}{h} (1 - z^{-1})}{1 + z^{-1} + T_d \frac{2}{N} (1 - z^{-1})}
\]

\[
\frac{(1 + T_d \frac{2}{h}) + (1 - T_d \frac{2}{h}) z^{-1}}{(1 + \frac{T_d}{N} \frac{2}{h}) + (1 - \frac{T_d}{N} \frac{2}{h}) z^{-1}}
\]

\[
u(k) = clu(k-1) - d0y(k) - d1y(k-1)
\]

\[
u(z)(1 + c1z^{-1}) = y(z)(d0 + d1z^{-1})
\]

From the above equation, the parameters are $c1$, $d0$, and $d1$ which are used as a part in matlab tool for obtaining the poles for this second order PD controller system.

6.2 calculations for finding the poles of PD controller of Second order:

Computation of the coefficients of the Digital Controller:
\[
H(z^{-1}) = \left( \frac{B(z^{-1})}{A(z^{-1})} \right)
\]

\[
= \left( \frac{b1z^{-1} + b2z^{-2}}{1 + a1z^{-1} + a2z^{-2}} \right)
\]
The above mentioned $B(z^{-1})$ & $A(z^{-1})$ are the transfer function of the second order PD controller

$$P(z^{-1}) = A(z^{-1})C(z^{-1}) + B(z^{-1})D(z^{-1}) \tag{70}$$

Equation 67 and 68 are substituted in 69 equation

$$= (1+a_1z^{-1} + a_2z^{-2}) (1+c_1z^{-1}) + (b_1z^{-1} + b_2z^{-2}) (d_0 + d_1z^{-1})$$

$C(z^{-1})$ & $D(z^{-1})$ are calculated from the second order PD controller by using Tustin method.

$$(1 + c_1z^{-1} + a_1z^{-1} + a_1c_1z^{-2} + a_2z^{-2} + a_2c_1z^{-3}) + (b_1d_0z^{-1} + b_1d_1z^{-2} + b_2d_0z^{-2} + b_2d_1z^{-3})$$

In the above equation $z^{-1}$, $z^{-2}$, and $z^{-3}$ are taken as common,

$$= 1 + z^{-1} (c_1 + a_1 + b_1d_0) + z^{-2} (a_1c_1 + a_2 + b_1d_1 + b_2d_0) + z^{-3} (a_2c_1 + b_2d_1) \tag{71}$$

Finding poles $p_1$, $p_2$, and $p_3$ in terms of $q_1$, $q_2$, and $q_3$

$$P(z) = (1 - q_1z^{-1})(1 - q_2z^{-1})(1 - q_3z^{-1})$$

$$= 1 + p_1z^{-1} + p_2z^{-2} + p_3z^{-3} \tag{72}$$

Then choose the poles $p_1$, $p_2$, and $p_3$

$$P_1 = -q_3 - q_2 - q_1 \tag{73}$$

$$P_2 = q_2q_3 + q_1q_3 + q_1q_2 \tag{74}$$

Identifying the coefficients for $p(z)$ from the equation 71.

Where $p_1 = c_1 + a_1 + b_1d_0$

$$p_2 = a_1c_1 + a_2 + b_1d_1 + b_2d_0 \text{ and}$$

$$p_3 = a_2c_1 + b_2d_1$$

From the above obtained $p_1$, $p_2$ and $p_3$; $d_0$, $d_1$ and $c_1$ parameters are extracted.

$$d_0 = \left(\frac{p_1 - c_1 - a_1}{b_1}\right), \quad d_1 = \left(\frac{p_3 - a_2c_1}{b_2}\right)$$

and for extracting $c_1$ parameter $d_0$ and $d_1$ values are
substituted in the p2 equation, as shown below

\[ p2 = alc1 + a2 + b1d1 + b2d0 \]

Simplification is shown in Appendix 6.

\[
c1 = \left( \frac{b1b2(p2 - a2) + b2^2(a1 - p1) - b1^2p3}{alb1b2 + b1^2a2 + b2^2} \right)
\]

For this controller we had tried the following different combinations of the poles.

The resulted graphs are seen in the Appendix-7.

At \(2e^{-6}\), the following pole combination are consider

q1 = 0.48, q2 = 0.46, & q3 = 0.47
q1 = 0.21, q2 = 0.24, & q3 = 0.25 and
q1 = 0.38, q2 = 0.36, & q3 = 0.37

At this output \(2e^{-6}\), and at poles q1 = 0.38, q2 = 0.36, & q3 = 0.37, the result is better than the remaining combinations, so we consider these pole values for both the controllers. And the same formula is used as in equation 26.
6.3 Simulation results of the Continuous and Discrete model of PD controller:

In the above figure we can observe that the d signal is constant with the pwm signal, from this we can say the system is stable with reference to the pwm signal. And now we can consider the output graphs for the continuous and discrete models.
In this simulation [continuous model] the reference is constant at 5.4v, which is nearer to the expected reference voltage 5. And the output voltage also is constant from same as the ‘d’ signal, it shows that the continuous model with PD controller works with little late response.

The controller output can get stable quickly [in 1.3 μs] and remain stable. What’s more, the controller make the output voltage come to stable status quickly [in 1.3 μs] and remain the wanted output voltage nearer to the reference voltage 5V.
It can be seen that the output voltage of discrete system can satisfy the required voltage. The controller output is stable from the 63 sampling interval. What’s more, the controller make the output voltage come to stable quickly and remain the wanted output voltage [reference voltage 5V].

The simulation is according to the polynomial controller theory, so it is an illustration of the theory by numerical calculations. When designing the controller, it should not hit the saturation limits too much for a reasonable disturbance. And the simulation result satisfies it.
6.4 Simulation result without disturbance and with noise:

In the real case, the circuit consists of some noise, at the switch according to high frequency switching, measurement noise and so on. Here, adding some band-limited white noise to simulate it. The noise magnitude is between 5.3 and 5.5, for the simulation results see the below figure.

![Output voltage measurement with noise, without disturbance](image)

**Fig 39:** output voltage measurement with noise, and without disturbance.

6.5 Simulation results without noise and with disturbance:

Here current source is used as the load, when the output current suddenly changes, then there will be some disturbance in the output voltage too.
To observe the disturbance and check whether the polynomial controller has enough ability to turn the output voltage back to reference voltage. Setting the current change range as 5A-15A, we can observe the simulation results below.

![Simulation result, with disturbance, without noise](image)

The stable output voltage under control is about 5.44 V which is quite close to the reference voltage 5V, and it means the controller is good.

At the 60th sample interval [180μs] changing the load current from 5A to 15A, the output voltage does not fall or rise the voltage, maintains same voltage 5.4 which very good because when the load changes suddenly the controller maintains the output voltage in a stable voltage.
Chapter 7. CONCLUSION:

In this paper, three different controllers are designed and proposed for Ericsson’s BMR450 [DC-DC Buck Converter]. A Simulink model of the buck converter has been designed to generate the output needed for buck system, to get the required output from the system, a reference voltage was used. Here reference voltage is the key parameter for checking the performance of these controllers, and the controller blocks are implemented by using Matlab and substantiated through simulation results.

The designing method is same for all the three controllers i.e., Polynomial Pole Placement, PID and PD. While the polynomial controller involves straightforward design using the plant $B(z)/A(Z)$ and parameters $c_1$, $c_2$, $d_0$, $d_1$ and $d_2$. But in the digital PID controller, we have considered the design using three parameters - proportional, integral and derivative. And the parameters of C and D i.e., $r_0$, $r_1$, $r_2$, $s_1$ and $s_2$ are expressed in terms of PID, five parameters as $c_1$, $c_2$, $d_0$, $d_1$ and $d_2$ in PID we used same parameters as $r_0$, $r_1$, $r_2$, $s_1$ and $s_2$. And for the PD controller we have considered the design using two parameters – proportional and derivative, here we have $c_1$, $d_0$ and $d_1$.

As per the results point of view, the polynomial performed well than PID and PD controllers. The simulations show that the polynomial controller reaches the reference voltage very well, were the PID and PD result does not differ very much while meeting with the required reference voltage. Thus we conclude that the Polynomial controller design and results were better than the PID and PD Controllers.

If we compare both the second order [4] and third order controller methods, The second order controllers are easy in design and gives better responses than third order polynomial PID and PD controllers.

7.1 Future work:

The performance could be tested by using few other controllers like RLS [Recursive Least Square] algorithm, LMS [Least Mean Square] algorithm. And the PID, PD Controller could be redesigned by using root locus method. The reduced order model could be used for testing the behavior of the buck converter.
Chapter 8. References:


Appendix-1: Mathematical equation of buck converter when switch is ON. (Page-16)

Matrix 4: Division of the matrix to transform into $\dot{x} = Ax + Bu$.

So from the above matrix $[8\times 8]$, transforming it to $\dot{x} = Ax + Bu$ equation, in Matrix 4 were the red and black lines meet those values are taken to represent the below matrix in $[3\times 3]$ for $u_{C1}$, $u_{C2}$, $i_l$. And for the $u_{RL}$, $u_{RL}$, $i_{R2}$ matrices are taken from the above Matrix 4, were the green and black lines meet each other. The last matrix is $u_U$ and $i_i$ $[2\times 3]$ is consider from above matrix were $i_{C1}$ $[2\times 1, 2\times 8]$, $i_{C2}$ $[3\times 1, 3\times 8]$, $i_l$ $[7\times 1, 7\times 8]$.

\[
\begin{bmatrix}
C_1 & 0 & 0 \\
0 & C_2 & 0 \\
0 & 0 & L
\end{bmatrix}
\begin{bmatrix}
\cdot u_{C1} \\
\cdot u_{C2} \\
\cdot i_l
\end{bmatrix}
= 
\begin{bmatrix}
0 & 0 & 1 \\
0 & 0 & 0 \\
-1 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
u_{C1} \\
u_{C2} \\
i_l
\end{bmatrix}
+ 
\begin{bmatrix}
0 & 0 & -1 \\
0 & 0 & 1 \\
-1 & -1 & 0
\end{bmatrix}
\begin{bmatrix}
u_{RL} \\
u_{RL} \\
i_{R2}
\end{bmatrix}
+ 
\begin{bmatrix}
0 & 0 \\
0 & 1 \\
-1 & 0
\end{bmatrix}
\begin{bmatrix}
u_U \\
u_U \\
i_i
\end{bmatrix}
\]

(1)

\[
\begin{bmatrix}
i_{R1} \\
i_{R2}
\end{bmatrix}
= 
\begin{bmatrix}
1 / R_L & 0 & 0 \\
0 & 1 / R_1 & 0 \\
0 & 0 & R_2
\end{bmatrix}
\begin{bmatrix}
u_{RL} \\
u_{RL} \\
i_{R2}
\end{bmatrix}
\]

(2)

By substituting the equation 3 in 2, we obtain
\[
\begin{bmatrix}
1/R_L & 0 & 0 \\
0 & 1/R_1 & 0 \\
0 & 0 & R_2
\end{bmatrix}
\begin{bmatrix}
u_{rL} \\
u_{r1} \\
i_{r2}
\end{bmatrix}
=
\begin{bmatrix}
0 & 0 & 1 \\
1 & -1 & 0
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\
u_{c2} \\
i_L
\end{bmatrix}
+ \begin{bmatrix}
0 & 0 & 0 \\
0 & 0 & -1
\end{bmatrix}
\begin{bmatrix}
u_{rL} \\
u_{r1} \\
i_{r2}
\end{bmatrix}
+ \begin{bmatrix}
0 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
u_{w} \\
i_t
\end{bmatrix}
\tag{4}
\]

Solving the above equation in the following steps, as show below.

\[
\begin{bmatrix}
0 & 0 & 0 \\
0 & 1/R_1 & 0 \\
0 & -1/R_2 & 0
\end{bmatrix}
\begin{bmatrix}
u_{rL} \\
u_{r1} \\
i_{r2}
\end{bmatrix}
=
\begin{bmatrix}
0 & 0 & 1 \\
0 & 0 & 1 \\
1 & -1 & 0
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\
u_{c2} \\
i_L
\end{bmatrix}
+ 
\begin{bmatrix}
0 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
u_{w} \\
i_t
\end{bmatrix}
\tag{5}
\]

\[
\begin{bmatrix}
u_{rL} \\
u_{r1} \\
i_{r2}
\end{bmatrix}
=
\begin{bmatrix}
R_L & 0 & 0 \\
0 & R_1/(R_1+R_2) & -R_1/(R_1+R_2) \\
0 & 1/(R_1+R_2) & 1/(R_1+R_2)
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\
u_{c2} \\
i_L
\end{bmatrix}
+ \begin{bmatrix}
0 & 0 & 0 \\
0 & 0 & -1
\end{bmatrix}
\begin{bmatrix}
u_{w} \\
i_t
\end{bmatrix}
\tag{6}
\]

Substituting equation 5 in equation 6, the resulted equation is as show below.
\[
\begin{bmatrix}
0 & 0 & R_L \\
-\frac{1}{R_1 + R_2} & \frac{1}{R_1 + R_2} & \frac{R_1 R_2}{(R_1 + R_2)^2} \\
1/(R_1 + R_2) & -1/(R_1 + R_2) & R_1/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\ u_{c2} \\ i_L
\end{bmatrix}
+ \begin{bmatrix}
0 & 0 \\
-\frac{1}{R_1 R_2 (R_1 + R_2)} & \frac{1}{R_1 R_2 (R_1 + R_2)} \\
1/(R_1 R_2 / (R_1 + R_2)) & -1/(R_1 R_2 / (R_1 + R_2)) & -R_L / (R_1 R_2 / (R_1 + R_2))
\end{bmatrix}
\begin{bmatrix}
u_{w} \\ i_i
\end{bmatrix}
\]

the below equation

\[
\begin{bmatrix}
i_{c1} \\ i_{c2} \\ u_L
\end{bmatrix}
= \begin{bmatrix}
0 & 0 & 1 \\
0 & 0 & 0 \\
-1 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\ u_{c2} \\ i_L
\end{bmatrix}
+ \begin{bmatrix}
-1/(R_1 + R_2) & 1/(R_1 + R_2) & -1/(R_1 + R_2) \\
1/(R_1 + R_2) & -1/(R_1 + R_2) & R_1/(R_1 + R_2) \\
R_1 R_2 / (R_1 + R_2) & -R_1 R_2 / (R_1 + R_2) & -R_L - R_1 R_2 / (R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
u_{w} \\ i_i
\end{bmatrix}
= \begin{bmatrix}
0 & 0 \\
-1 & 0 \\
0 & 0
\end{bmatrix}
\begin{bmatrix}
u_{w} \\ i_i
\end{bmatrix}
\]

\[
\begin{bmatrix}
i_{c1} \\ i_{c2} \\ u_L
\end{bmatrix}
= \begin{bmatrix}
0 & 0 & 1 \\
0 & 0 & 0 \\
-1 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
u_{c1} \\ u_{c2} \\ i_L
\end{bmatrix}
+ \begin{bmatrix}
-1/(R_1 + R_2) & 1/(R_1 + R_2) & 1 - R_1/(R_1 + R_2) \\
1/(R_1 + R_2) & -1/(R_1 + R_2) & R_1/(R_1 + R_2) \\
R_1 R_2 / (R_1 + R_2) & -R_1 R_2 / (R_1 + R_2) & -R_L - R_1 R_2 / (R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
u_{w} \\ i_i
\end{bmatrix}
\]

(7)
The above equation is the state space model of the signal.

The output voltage equation is

\[ u_0 = u_{R1} + u_{C1} \]

Here considering the \( U_{R1} \) from the 3 matrix row in equation 5 and \( U_{C1} \) matrix from equation 6

\[
\begin{bmatrix}
    u_{R1} \\
    u_{R2} \\
    i_{R2}
\end{bmatrix}
= \begin{bmatrix}
    0 & 0 & R_L \\
    -R_1/(R_1 + R_2) & R_1/(R_1 + R_2) & R_1R_2/(R_1 + R_2) \\
    1/(R_1 + R_2) & -1/(R_1 + R_2) & R_1/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    u_{C1} \\
    u_{C2} \\
    i_L
\end{bmatrix}
+ \begin{bmatrix}
    0 & 0 \\
    0 & -R_1R_2/(R_1 + R_2) \\
    0 & -R_1/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    u_{W} \\
    i_{L}
\end{bmatrix}
\]

\[ u_{R1} = \left[ \begin{bmatrix}
    u_{W} \\
    i_{R2}
\end{bmatrix} + U_{C1} \right]
\]

\[ u_{R1} = \begin{bmatrix}
    1 - R_1/(R_1 + R_2) & R_1/(R_1 + R_2) & R_1R_2/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    u_{C1} \\
    u_{C2} \\
    i_L
\end{bmatrix}
+ \begin{bmatrix}
    0 & -R_1R_2/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    u_{W} \\
    i_{R2}
\end{bmatrix}
\]

And changing \( u \) into \( v \) for the input voltage \( [v= -u] \).

\[
\begin{bmatrix}
    i_{C1} \\
    i_{C2} \\
    u_{L}
\end{bmatrix}
= \begin{bmatrix}
    -1/(R_1 + R_2) & 1/(R_1 + R_2) & 1 - R_1/(R_1 + R_2) \\
    1/(R_1 + R_2) & -1/(R_1 + R_2) & R_1/(R_1 + R_2) \\
    -1 + R_1/(R_1 + R_2) & -R_1/(R_1 + R_2) & -R_1 - R_1R_1/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    u_{C1} \\
    u_{C2} \\
    i_L
\end{bmatrix}
\]

\[
\begin{bmatrix}
    0 & -1 + R_1/(R_1 + R_2) \\
    0 & -R_1/(R_1 + R_2) \\
    1 & R_1R_2/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    v \\
    i_{R2}
\end{bmatrix}
\]

(8)
System equation:

\[
\begin{bmatrix}
    u_{c1} \\
    u_{c2} \\
    i_l
\end{bmatrix} =
\begin{bmatrix}
    -1/(R_1 + R_2)C_1 & 1/(R_1 + R_2)C_1 & 1/C_1 - R_1/(R_1 + R_2)C_1 \\
    1/(R_1 + R_2)C_2 & -1/(R_1 + R_2)C_2 & R_1/(R_1 + R_2)C_2 \\
    -1/L + R_1/(R_1 + R_2) & -R_1/(R_1 + R_2)L & -R_L - R1R1/(R_1 + R_2)L
\end{bmatrix}
\begin{bmatrix}
    u_{c1} \\
    u_{c2} \\
    i_l
\end{bmatrix} +
\begin{bmatrix}
    v \\
    i_l
\end{bmatrix}
\]

(9)

Output equation:

\[
\begin{bmatrix}
    u_{c1} \\
    u_{c2} \\
\end{bmatrix} =
\begin{bmatrix}
    1 - R_1/(R_1 + R_2) & R_1/(R_1 + R_2) & R_1R_2/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    u_{c1} \\
    u_{c2} \\
    i_l
\end{bmatrix} +
\begin{bmatrix}
    0 & -R_1R_2/(R_1 + R_2)
\end{bmatrix}
\begin{bmatrix}
    w \\
    i_l
\end{bmatrix}
\]

(10)
Appendix -2: Solving the equation for obtaining the Polynomial pole placement. (Page-21)

Recalling equation 22, and identifying the polynomials AC+BD with P.

\[ C(z) = 1 + c_1z^{-1} + c_2z^{-2} \]  \hspace{1cm} \text{(33)}

\[ P_1 = c_1 + a_1 + b_1d_0 \]
\[ P_2 = c_2 + a_2 + a_1c_1 + b_1d_1 + b_2d_0 \]
\[ P_3 = a_1c_2 + a_2c_1 + a_3 + b_1d_2 + b_2d_1 + b_3d_0 \]
\[ P_4 = a_2c_2 + a_3c_1 + b_2d_2 + b_3d_1 \]
\[ P_5 = a_3c_2 + b_3d_2 \]

Solve the above equations by using matrix method.

\[
\begin{pmatrix}
1 & 0 & b_1 & 0 & 0 \\
 a_1 & 1 & b_2 & b_1 & 0 \\
 a_2 & a_1 & b_3 & b_2 & b_1 \\
 a_3 & a_2 & 0 & b_3 & b_2 \\
 0 & a_3 & 0 & 0 & b_3 \\
\end{pmatrix}
\begin{pmatrix}
c_1 \\
c_2 \\
d_0 \\
d_1 \\
d_2 \\
\end{pmatrix}
= \begin{pmatrix}
-a_1 + (-q_5 - q_4 - q_3 - q_2 - q_1) \\
-a_2 + q_4q_5 + q_3q_5 + q_3q_4 + q_2q_5 + q_2q_4 + q_2q_3 + q_1q_5 + c_1q_4 + c_1q_3 + c_1q_2 \\
-q_3q_5 - q_1q_3q_4 - q_1q_2q_5 - q_1q_2q_4 - q_1q_2q_3 \\
q_2q_3q_4q_5 + q_1q_3q_4q_5 + q_1q_2q_4q_5 + q_1q_2q_3q_5 + q_1q_2q_3q_4 \\
-q_1q_2q_3q_4q_5 \\
\end{pmatrix}
\]

\hspace{1cm} \text{(34)}

For simplicity let matrix \( M = \begin{pmatrix} 1 & 0 & b_1 & 0 & 0 \\ a_1 & 1 & b_2 & b_1 & 0 \\ a_2 & a_1 & b_3 & b_2 & b_1 \\ a_3 & a_2 & 0 & b_3 & b_2 \\ 0 & a_3 & 0 & 0 & b_3 \end{pmatrix} \) and \( F = \begin{pmatrix} c_1 \\ c_2 \\ d_0 \\ d_1 \\ d_2 \end{pmatrix} \)
\[
N = \begin{pmatrix}
-a_1 + (-q_5 - q_4 - q_3 - q_2 - q_1) \\
-a_2 + q_4q_5 + q_3q_5 + q_3q_4 + q_2q_5 + q_2q_4 + q_2q_3 + q_1q_5 + q_1q_4 + q_1q_3 + q_1q_2 \\
-a_3 + (-q_3q_4q_5 - q_2q_4q_5 - q_2q_3q_5 - q_2q_3q_4 - q_1q_4q_5 - q_1q_3q_5 - q_1q_2q_5 - q_1q_2q_4 - q_1q_2q_3) \\
q_2q_3q_4q_5 + q_1q_3q_4q_5 + q_1q_2q_4q_5 + q_1q_2q_3q_5 + q_1q_2q_3q_4 - q_1q_2q_3q_4
\end{pmatrix}
\]
Appendix -3: Resulted graphs for determining the pole values in polynomial controller. (Page-23)

When the poles are $q_1 = 0.1$, $q_2 = 0.2$, $q_3 = 0.3$, $q_4 = 0.4$, $q_5 = 0.5$. And at Sampling interval $6e^{-6}$ the below graph is obtained which is not stable.

And at the poles $q_1 = 0.3$, $q_2 = 0.4$, $q_3 = 0.5$, $q_4 = 0.6$, $q_5 = 0.7$ at the sampling interval $6e^{-6}$ the resulted graph also not stable, as the graph with poles taken with 0.5 values.
Figure 35: Simulation of polynomial controller at poles 0.3, 0.4, 0.5, 0.6, and 0.7 at the sampling interval $6e^{-6}$.

From the above resulted Figures 34 and 35, we had decided that, the consider poles values are not correct to stabilize the Polynomial Pole Placement controller. So we had taken the pole values to be 0.5, 0.5, 0.5, 0.5, and 0.5 at sampling interval $[6e^{-6}]$ is better.
Appendix -4: Solving the equation for obtaining the PID. (Page-41)

Multiplying the above equation 58, gives

\[ 1 + s1z^{-1} + s2z^{-2} + a1z^{-1} + a1s1z^{-2} + a1s2z^{-3} + a2z^{-2} + a2s1z^{-3} + a2s2z^{-4} + a3z^{-3} + a3s1z^{-4} + a3s2z^{-5} + b1r0z^{-1} + b1r1z^{-2} + b1r2z^{-3} + b2r0z^{-2} + b2r1z^{-3} + b2r2z^{-4} + b3r0z^{-3} + b3r1z^{-4} + b3r2z^{-5} \]

Taking \( s \) as common in above equation.

\[ 1 + (s1 + a1 + b1r0)z^{-1} + (s2 + a1s1 + a2 + b1r1 + b2r0)z^{-2} + (a1s2 + a2s1 + a3 + b1r2 + b2r1 + b3r0)z^{-3} + (a2s2 + a3s1 + b2r2 + b3r1)z^{-4} + (a3s2 + b3r2)z^{-5} \]

Finding poles P1, P2, P3, P4, and P5 in terms of q1, q2, q2, q4 and q5.

\[ P(z^{-1}) = (1 - q1z^{-1})(1 - q2z^{-1})(1 - q3z^{-1})(1 - q4z^{-1})(1 - q5z^{-1}) = 1 + p1z^{-1} + p2z^{-2} + p3z^{-3} + p4z^{-4} + p5z^{-5} \]  

(59)

Then poles would be,

\[ P1 = -q5 - q4 - q3 - q2 - q1 \]  

(60)

\[ P2 = q4q5 + q3q5 + q3q4 + q2q5 + q2q4 + q2q3 + q1q5 + q1q4 + q1q3 + q1q2 \]  

(61)

\[ P3 = -q3q4q5 - q2q4q5 - q2q3q5 - q2q3q4 - q1q4q5 - q1q3q5 - q1q3q4 - q1q2q5 - q1q2q4 - q1q2q3 \]  

(62)

\[ P4 = q2q3q4q5 + q1q3q4q5 + q1q2q4q5 + q1q2q3q5 + q1q2q3q4 \]  

(63)

\[ P5 = -q1q2q3q4q5 \]  

(64)

Recalling equation 27, and identifying the polynomial AC+BD with P.
\( P1 = s1 + a1 + b1r0 \)
\( P2 = s2 + a2 + a1s1 + b1r1 + b2r0 \)
\( P3 = a1s2 + a2s1 + a3 + b1r2 + b2r1 + b3r0 \)
\( P4 = a2s2 + a3s1 + b2r2 + b3r1 \)
\( P5 = a3s2 + b3r2 \)

Solve the above equations by using matrix method.

\[
\begin{pmatrix}
1 & 0 & b1 & 0 & 0 \\
a1 & 1 & b2 & b1 & 0 \\
a2 & a1 & b3 & b2 & b1 \\
a3 & a2 & 0 & b3 & b2 \\
0 & a3 & 0 & 0 & b3 \\
\end{pmatrix}
\begin{pmatrix}
s1 \\
s2 \\
r0 \\
r1 \\
r2 \\
\end{pmatrix}
= -a1 + (-q5 - q4 - q3 - q2 - q1)
- a2 + q4q5 + q3q5 + q3q4 + q2q5 + q2q4 + q2q3 + q1q5 + q1q4 + q1q3 + q1q2
- a3 + (-q3q4q5 - q2q4q5 - q2q3q5 - q2q3q4 - q1q4q5 - q1q3q5 - q1q2q5 - q1q2q4 - q1q2q3)
q2q3q4q5 + q1q3q4q5 + q1q2q4q5 + q1q2q3q5 + q1q2q3q4
- q1q2q3q4q5
\]

(65)

The Matlab notation I used is \( F * M = J \) \( \Rightarrow F = \text{inv}(M) * J \), from this we can get the poles.
Appendix 5: Resulted graphs for determining the poles for the PID Controller. (Page-40)

When the poles are $q_1 = 0.1$, $q_2=0.2$, $q_3=0.3$, $q_4=0.4$, $q_5=0.5$. And at sampling interval $1e^{-6}$ the below graph is obtained which is not stable.

![Switched continuous model with saturation](image)

Figure 36: simulation of PID controller at poles 0.1, 0.2, 0.3, 0.4, and 0.5 at the sampling interval $1e^{-6}$.

And at the poles $q_1 = 0.3$, $q_2 = 0.4$, $q_3 = 0.5$, $q_4 = 0.6$, $q_5 = 0.7$ at the sampling interval $1e^{-6}$ the resulted graph also not stable, as the graph with poles taken with 0.5 values.
Figure 37: simulation of PID controller at poles 0.3, 0.4, 0.5, 0.6, and 0.7 at the sampling interval $1e^{-6}$.

From the above resulted Figures 36 and 37, we had decided that, the consider poles values for the PID controller are not correct to stabilize. So we had taken the pole values to be 0.5, 0.5, 0.5, 0.5, and 0.5 at sampling interval $[1e^{-6}]$ is better.
Appendix -6: Solving the equation for obtaining the PD (page-54)

\[ p_2 = \left( a_1c_1 + a_2 + b_1 \left( \frac{p_3 - a_2c_1}{b_2} \right) \right) + b_2 \left( \frac{p_1 - c_1 - a_1}{b_1} \right) \]  \hfill (76)

\[ p_2 = \left( \frac{a_1c_1b_2b_1 + a_2b_2b_1 + b_1^2 p_3 - b_1^2 a_2c_1 + b_2^2 p_1 - c_1b_2^2 - a_1b_2^2}{b_1b_2} \right) \]  \hfill (77)

\[ p_2b_1b_2 = c_1(a_1b_2b_1 - b_1^2 a_2 - b_2^2) + a_2b_2b_1 + b_1^2 p_3 + b_2^2 p_1 - a_1b_2^2 \]  \hfill (78)

\[ c_1 = \left( \frac{b_1b_2 p_2 - a_2b_2b_1 - b_1^2 p_3 + b_2^2 (a_1 - p_1)}{a_1b_1b_2 - b_1^2 a_2 + b_2^2} \right) \]  \hfill (79)

\[ c_1 = \left( b_1b_2(p_2 - a_2) + b_2^2 (a_1 - p_1) - b_1^2 p_3 \right) \]  \hfill (80)
Appendix -7: Resulted graphs for determining the pole values in PD controller. (page54)

In the above graph, $v_{out}$ is constant 7.7 with oscillations, it doesn’t fulfill our required expectations with the poles 0.48, 0.46 & 0.47 at sampling interval $2^{-6}$, and if we consider the simulation results of the discrete model with saturation here $v_{out}$ is increasing up to 100k while the d signal is constant from initial, so from observing these graphs [switched and discrete] we can observer that the reference value is not constant and is not at the required voltage 5.
If we consider the poles at 0.21, 0.24 & 0.25 at sampling interval $2^{-5}$, then the following are the resulted graphs.

---

**Discrete model with saturation**

- **Output voltage**
- **Sampling sequence k**

**Discrete polynomial controlled Buck, switched continuous model with saturation**

- **$dVg$**
- **d**
- **PWM**

---

84
In the above figure at those poles, the d value is not constant with the pwm signal, so from this simulation we can observer that at these poles the system is not stable.

Here the reference voltage 3.9, which is very good result but the d value is not constant with the pwm and $V_{out}$ is oscillating more.

The below discrete simulation the d and the $V_{out}$ are shown which are good, but in the above graph the d value is not constant, so the below is not valid.
Discrete model with saturation

Output voltage

Sampling sequence k

Vout

d
Appendix 8: Matlab codes for Polynomial Pole Placement Controller:

%% BMR450 Simple controller
clear all
close all
clc

%% DC-DC Converter System Design
L  = 0.9e-6;
C1 = 150e-6;  %150e-6;
RL = 10e-3;
R1 = 5e-3;

%Load resistor
R2 = 100e-3;
% Load capacitor.
C2=300e-3;
Vg = 12;

I_initial = 5;  % Load Current; 10
I_final = 15;
% Sampling interval for the controller
h = 6e-6;      %/100;%20
% pwm signal frequency
fs = 1/h;
% Sampling interval for the pwm generator
hpwm = h/100;
% Set point
yRef = 3.3;
I_ssteptime =h;
%noise = 1e-10;
noise = 0;

% Model single input
A = [-1/(C1*(R1+R2)) 1/(C1*(R1+R2)) ((1/C1)-(R1/(C1*(R1+R2)))) ;1/(C2*(R1+R2)) -1/(C2*(R1+R2)) R1/(C2*(R1+R2));(-1/L)+(R1/(L*(R1+R2))) -R1/(L*(R1+R2)) (-RL)-((R1*R2)/(L*(R1+R2)))];
% Input signals duty cycle and load current
B1 = [0 (-1/C1) R1/(C1*(R1+R2));0 -R1/(C2*(R1+R2));1/L (R1*R2)/(L)*(R1+R2)];% With power supply
B0 = [ 0;0;0];            % Without power supply
B = B1*Vg;  %averaged system, the control signal is duty cycle d
C = [(1-R1)/R1*(R1+R2)) (R1/R1*(R1+R2)) (R1+R2)/R1*(R1+R2)];  
D = [0 - (R1*R2)/R1*(R1+R2);]

%Model
A2=[-1/(C1*(R1+R2)) 1/(C1*(R1+R2)) ((1/C1)-(R1/(C1*(R1+R2)))) ;1/(C2*(R1+R2)) -1/(C2*(R1+R2)) R1/(C2*(R1+R2));(-1/L)+(R1/(L*(R1+R2))) -R1/(L*(R1+R2)) (-RL)-((R1*R2)/(L*(R1+R2)))];
B2=[0 (-1/C1) R1/(C1*(R1+R2));0 -R1/(C2*(R1+R2));1/L (R1*R2)/(L)*(R1+R2)];
C2=[(1-R1)/R1*(R1+R2)) (R1/R1*(R1+R2)) (R1+R2)/R1*(R1+R2)];
D2=[0 - (R1*R2)/R1*(R1+R2)];
% Initial values
c0 = 0;
c20 = 0;
iL0 = 0;

% Continuous system
Gcss = ss(A,B,C,D);
[Ac,Bc,Cc,Dc]=ssdata(Gcss)
Gctf=tf(Gcss);
[Bctf,Actf]=tfdata(Gctf(1),'v')

% Discrete system
Gdss=c2d(Gcss,h,'zoh');
[Ad,Bd,Cd,Dd]=ssdata(Gdss)
Gdtf=tf(Gdss);
[Bdtf,Adtf]=tfdata(Gdtf(1),'v')

% polynomial coefficients, index means # delays
Bd1 = Bdtf(2);
Bd2 = Bdtf(3);
Bd3 = Bdtf(4);
Ad0 = Adtf(1);
Ad1 = Adtf(2);
Ad2 = Adtf(3);
Ad3 = Adtf(4);

b1=Bd1;
b2=Bd2;
b3=Bd3;
a0=Ad0;
a1=Ad1;
a2=Ad2;
a3=Ad3;

q1=0.5;
q2=0.5;
q3=0.5;
q4=0.5;
q5=0.5;

% F is the MATRIX containing coefficients for c1,c2,d0,d1,d2
% J is the MATRIX containing POLES and Constants
K=[1 0 b1 0 0; a1 1 b2 b1 0; a2 a1 b3 b2 b1; a1 a2 0 b3 b2; 0 a3 0 0 b3];

L=[(-a1-(q1 + q2 + q3 + q4 + q5));(-a2-(q1*q2 -q1*q3-q1*q4-q2*q3-q1*q5-
q2*q4-q2*q5-q3*q4-q3*q5-q4*q5));(-a3+(-q3*q4*q5-q2*q4*q5-q3*q4-
q4*q5-q1*q3*q5-q1*q4-q3*q5-q1*q4-q3*q5-q1*q4-q3*q5-q1*q4-
q2*q3*q4+q1*q2*q4+q1*q2*q5+q1*q2*q3*q5+q1*q2*q3*q4);(-q1*q2*q3*q4*q5));

% I is the resultant matrix
M=inv(K)*L;
%finding values using poleplacement
\[ c_1 = M(1,1) \]
\[ c_2 = M(2,1) \]
\[ d_0 = M(3,1) \]
\[ d_1 = M(4,1) \]
\[ d_2 = M(5,1) \]

% C polynomial and D polynomial
\[ \text{cpoly} = [1 \ c_1 \ c_2] \]
\[ \text{dpoly} = [d_0 \ d_1 \ d_2] \]

% design of Stationary Gain
\[ K = \frac{D(1) + A(1) C(1) \cdot B(1)}{B(1)} = d_0 + d_1 + (1 + a_1 + a_2) \cdot (1 + c_1) \quad (b_1 + b_2) \]
\[ K_r = \frac{d_0 + d_1 + d_2 + (1 + a_1 + a_2 + a_3) \cdot (1 + c_1 + c_2)}{(b_1 + b_2 + b_3)} \]

% Simulation
% % Simulation
N = 100; % 100 sample intervals
\text{sim('pBuckswitch',N*h); } % PWM model with saturation
% %
\text{figure(6)}
\text{plot(ts,Vout,ts(1:length(d))),d,ts,iL/10)}
\text{legend('Vout','d','iL/10')}
\text{grid}
\text{title('Switched continuous model with saturation')}
\text{title('Simulation result, without disturbance, without noise')}
\text{xlabel('Time'),ylabel('Output voltage, Inductor current, Controller output')}

\text{figure(7)}
\text{plot(ts,Vout,ts(1:length(d))),d,ts,pwm)}
\text{legend('Vout','d','PWM')}
\text{grid}
\text{title('simulation results, with disturbance and without noise')}
\text{xlabel('Time'),ylabel('Output voltage, Controller output, PWM wave')}

\text{figure(8)}
\text{plot(ts(1:5001),dVg(1:5001)*12,ts(1:5001),d(1:5001),ts(1:5001),pwm(1:5001))}
\text{legend('dVg','d','PWM')}\text{title('Discrete polynomial controlled Buck, switched continuous model with saturation')}
\text{xlabel('Time'),ylabel('controlled input voltage, controller output, PWM wave')}

\text{figure(9)}
\text{plot(ts(1:2001),d(1:2001),ts(1:2001),pwm(1:2001))}
\text{legend('d','PWM')}\text{title('Controller output & PWM wave')}
\text{xlabel('Time'),ylabel('Controller output, PWM wave')}

\text{figure(10)}
\text{plot(ts(1:10000),Noise(1:10000))}
\text{title('Noise Magnitude')}
\text{xlabel('Time'),ylabel('Noise')}
figure(11)
plot(ts(1:10000),Vout_N(1:10000))
legend('Without Noise')
title('Output voltage measurement without noise, without disturbance')
xlabel('Time'), ylabel('Measurement Noise')
Appendix 9: Matlab code for PID Controller:

```matlab
%% BMR450 Simple controller (Digital PID 1 controller)
clear all
close all
clc

%% DC-DC Converter System Design
L = 0.9e-6;
C1 = 30e-6; %30e-6;
RL = 2e-3; %2e-3;%50e-3;
R1 = 10e-3; %10e-3;%50e-3;

% Load resistor
R2 = 100e-3;%100e-3;%20e-3;

% Load capacitor.
C2 = 2e-3;%2e-3;

Vg = 12;

I_initial = 5; % Load Current; 10
I_final = 15;

% Sampling interval for the controller
h = 1e-6; %1e-6;    %/100;%20
% pwm signal frequency
fs = 1/h;

% Sampling interval for the pwm generator
hpwm = h/100;

% Set point
yRef = 3.3;

I_steptime = h;
noise = 0;

% Model single input
A = [-1/C1*(R1+R2)           (1/C1)*(R1+R2)       (1/C1)-R1/C1*(R1+R2);
     (1/C2)*(R1+R2)          (-1/C2)*(R1+R2)       (R1/C2)*(R1+R2);
     (-1/L)+R1/L*(R1+R2)     (-R1/L)*(R1+R2)       (-RL)-(R1*R2)/L*(R1+R2)];

% Input signals duty cycle and load current
B1 = [0  (-1/C1) + (R1/C1)*(R1+R2);
      0          (-R1/C2)*(R1+R2);
      1/L        (R1*R2/L)*(R1+R2)];

With power supply
```
\[ B_0 = [0; 0; 0]; \quad \text{% Without power supply} \]

\[ B = B_1 \cdot V_g; \quad \text{% averaged system, the control signal is duty cycle } d \]

\[ C = [1 - R_1/R_1 \times (R_1+R_2) \quad R_1/R_1 \times (R_1+R_2) \quad (R_1+R_1)/R_1 \times (R_1+R_2)]; \]

\[ D = [0 - (R_1 \times R_2)/R_1 \times (R_1+R_2)]; \]

\text{% Model}\n
\[ A_2 = \begin{bmatrix}
-1/C_1 \times (R_1+R_2) & (1/C_1) \times (R_1+R_2) & (1/C_1) - R_1/C_1 \times (R_1+R_2) \\
(1/C_2) \times (R_1+R_2) & (-1/C_2) \times (R_1+R_2) & (R_1/C_2) \times (R_1+R_2); \\
(-1/L) + R_1/L \times (R_1+R_2) & (-R_1/L) \times (R_1+R_2) & (-R_1) - (R_1 \times R_2)/L \times (R_1+R_2) \\
\end{bmatrix}; \]

\[ B_2 = \begin{bmatrix}
0 & (-1/C_1) + (R_1/C_1) \times (R_1+R_2); \\
0 & (-R_1/C_2) \times (R_1+R_2); \\
1/L & R_1 \times R_2/L \times (R_1+R_2); \\
\end{bmatrix}; \]

\[ C_2 = [1 - R_1/R_1 \times (R_1+R_2) \quad R_1/R_1 \times (R_1+R_2); \]

\[ D_2 = [0 - (R_1 \times R_2)/R_1 \times (R_1+R_2)]; \]

\text{% Initial values}\n
\[ u_{C0} = 0; \]

\[ u_{C20} = 0; \]

\[ i_{L0} = 0; \]

\text{% Continuous system}\n
\[ G_{css} = \text{ss}(A, B, C, D); \]

\[ \text{[Ac, Bc, Cc, Dc]} = \text{ssdata}(G_{css}); \]

\[ \text{Gctf} = \text{tf}(G_{css}); \]

\[ \text{[Bctf, Actf]} = \text{tfdata}(G_{ctf}(1), 'v'); \]

\text{% Discrete system}\n
\[ G_{dss} = \text{c2d}(G_{css}, h, 'zoh'); \]

\[ \text{[Ad, Bd, Cd, Dd]} = \text{ssdata}(G_{dss}); \]

\[ \text{Gdtf} = \text{tf}(G_{dss}); \]

\[ \text{[Bdtf, Adtf]} = \text{tfdata}(G_{dtf}(1), 'v'); \]

\text{% polynomial coefficients, index means # delays}\n
\[ b_1 = B_{dtf}(2); \]

\[ b_2 = B_{dtf}(3); \]

\[ b_3 = B_{dtf}(4); \]

\[ a_0 = A_{dtf}(1); \]

\[ a_1 = A_{dtf}(2); \]

\[ a_2 = A_{dtf}(3); \]

\[ a_3 = A_{dtf}(4); \]
b3=bd3;
a0=ad0;
a1=ad1;
a2=ad2;
a3=ad3;

q1=0.5;
q2=0.5;
q3=0.5;
q4=0.5;
q5=0.5;

% F is the MATRIX containing coefficients for c1,c2,d0,d1,d2
% J is the MATRIX containing POLES and Constants

F=[1 0 b1 0 0;
a1 1 b2 b1; a1 a2 0 b3 b2; a3 0 0 b3];
J=[(-a1+1-(q1 + q2 + q3 + q4 + q5));(-a2+a1-(-q1*q2 - q1*q3-q1*q4-q2*q3-q1*q5-q2*q4-q2*q5-q3*q4-q3*q5-q4*q5-q5));(-a3+a2+(-q3*q4-q5-q2*q5-q2*q3-q3*q5-q2*q4-q2*q3-q3*q4-q3*q5-q4*q5-q5));(-a3+q2*q3*q4*q5 + q1*q3*q4*q5 + q1*q2*q3*q5 + q1*q2*q3*q4)];

M=inv(F)*J;

% finding values using poleplacement
s1 = M(1,1);
s2 = M(2,1);
r0 = M(3,1);
r1 = M(4,1);
r2 = M(5,1);

spoly = [ 1 s1 s2];
% spoly1=1;

rpoly = [ r0 r1 r2];

% Simulation

N = 1*100; % 100 sample intervals
% sim('pidBuckdl',N); % Discrete time model without saturation
% sim('pidBuckd2',N); % Discrete time model with saturation
% sim('pidBuckc1',N*h); % Continuous time model
sim('pidBuckswitch',N*h); % PWM model with saturation

figure(6)
plot(ts,Vout,ts(1:length(d)),d,ts,iL/10)
legend('Vout', 'd', 'iL/10')
grid

title('Switched continuous model with saturation')
title('Simulation result, with disturbance, with noise')
xlabel('Time'), ylabel('Output voltage, Inductor current, Controller output')

figure(7)
plot(ts,Vout,ts(1:length(d)),d,ts,pwm)
legend('Vout','d','PWM')
grid

title('Switched continuous model with saturation')
xlabel('Time'), ylabel('Output voltage, Controller output, PWM wave')

figure(8)
plot(ts(1:1001),dVg(1:1001)*12,ts(1:1001),d(1:1001),ts(1:1001),pwm(1:1001))
legend('dVg','d','PWM')
title('Discrete PID controlled Buck, switched continuous model with saturation')
xlabel('Time'), ylabel('controlled input voltage, controller output, PWM wave')

figure(9)
plot(ts(1:1:5001),d(1:5001),ts(1:5001),pwm(1:5001))
legend('d','PWM')
title('Controller output & PWM wave')
xlabel('Time'), ylabel('Controller output, PWM wave')

figure(10)
plot(ts(1:1:10000),Noise(1:10000))
title('Noise Magnitude')
xlabel('Time'), ylabel('Noise')

figure(11)
plot(ts(1:1:10000),Vout_N(1:10000))
legend('With Noise')
title('Output voltage measurement with noise, without disturbance')
xlabel('Time'), ylabel('Measurement Noise')
Appendix 10: Matlab code for PD Controller:

%%%% BMR450 Simple controller
clear all
close all
c1c

%% DC-DC Converter System Design
%% Circuit data
%% Analysis of 3rd and 2nd order Buck POL converter
%% AH 2011 Dec 16

L=0.9e-6;
RL=10e-3;
C1=150e-6;
R1=2e-3;
C2=470e-6;
%C2=1000e-6;
R2=10e-3;
Vg=12;

A3 = [-1/(C1*(R1+R2)) 1/(C1*(R1+R2)) (1/C1)-(R1/(C1*(R1+R2)));...
    1/(C2*(R1+R2)) -1/(C2*(R1+R2)) R1/(C2*(R1+R2));...
    (-1/L)+(R1/(L*(R1+R2))) -R1/(L*(R1+R2)) (-RL/L)+((R1*R2)/(L*(R1+R2)));

B3 = [0 -R2/(C1*(R1+R2));...
    0 -R1/(C2*(R1+R2));...
    Vg/L (R1*R2)/(L)*(R1+R2)];

C3 = [1-R1/(R1+R2) (R1/(R1+R2)) (R1*R2)/(R1+R2)];

D3= [0 (R1*R2)/(R1+R2)];
figure(1)
step(A3,B3(:,1),C3,D3(1))
figure(2)
bode (A3,B3(:,1),C3,D3(1))

R_L=60e-3;
C_1=680e-6;\\%\\%\\%----------------------------------here you can adapt-----------------\\
R_1=35e-3;\\%\\%\\%----------------------------------here you can adapt-----------------\\

Vg = 12; % Input Voltage
I_initial = 5; % Load Current; 10
I_final = 15;
% Sampling interval for the controller
h = 2e-6;
% pwm signal frequency
fs = 1/h;
% Samplinginterval for the pwm generator
hpwm = h/100;
% Set point
yRef = 5;
I_steptime = h;
% noise = 1e-10;
noi se = 0;

A2 = [0 1/C_1;-1/L -(R_L+R_1)/L];

B2 = [0 -1/C_1;Vg/L R_1/L];

C2 = [ 1 R_L ; 0 1 ];

D2 = [ 0 0 ; 0 0 ];

% Model two input in state space
AA = A2;
BB = B2;
CC = C2;
DD = D2;

% Initial values
uC0 = 0;
iL0 = 0;

% Continuous system
Gcss = ss(A2,B2(:,1),C2(1,:),D2(1,1));
[Ac,Bc,Cc,Dc] = ssdata(Gcss);
Gctf = tf(Gcss);
[Bctf,Actf] = tfdata(Gctf,'v')

% Discrete system
Gdss = c2d(Gcss,h,'zoh');
[Ad,Bd,Cd,Dd] = ssdata(Gdss);
Gdtf = tf(Gdss);
[Bdtf,Adtf] = tfdata(Gdtf,'v')

% plot of step response
% figure(1)
% step(Gctf)
% hold on
% step(Gdtf)
% hold off

% polynomial coefficients, index means # delays
Bd0 = Bdtf(1);
Bd1 = Bdtf(2);
Bd2 = Bdtf(3);
Ad0 = Adtf(1);
Ad1 = Adtf(2);
Ad2 = Adtf(3);

% Polynomial Controller Design a la Schmidtbauer
b1 = Bd1;
\begin{verbatim}
% Pole placement
q1 = 0.38;
q2 = 0.36;
q3 = 0.37;

% polynomial controller derivation using method from Schmidtbauer
p1 = -q1-q2-q3;
p2 = q1*q2 + q2*q3 + q1*q3;
p3 = -q1*q2*q3;

% p1 = a1 + c1 + b1*d0
% p2 = a1*c1 + a2 + b1*d1 + b2*d0
% p3 = a2*c1 + b2*d1

c1 = (-b1^2*p3 + b1*b2*(p2-a2) + b2^2*(p1-a1))/(a2*b1^2 + b2^2 + a1*b1*b2);
d0 = (p1-a1-c1)/b1;
d1 = (p3-a2*c1)/b2;

% C polynomial and D polynomial
cpoly = [ 1 c1 ];
dpoly = [ d0 d1 ];

% design of Stationary Gain
% Kr = D(1)+A(1)C(1)/B(1)=(d0+d1)+(1+a1+a2)(1+c1)/(b1+b2)
Kr = (d0+d1)+(1+a1+a2)*(1+c1)/(b1+b2);

% Calculate the static gain
% Definition of continuous system:
Csys=ss(AA,BB,CC,DD);
% Discretizing the system:
Dsys=c2d(Csys,h,'zoh');
% Definition of the transfer function for the controller:
Rsys=tf(dpoly,cpoly,h);
% Calculation of the closed system:
Msys=feedback(Dsys,Rsys,1,1,-1);
% the closed system has two inputs (d and I0) and two outputs (u0 and IL).
% Generation of all four step responses
step(Msys)
% calculation of all zeros, poles and static gains.
[z,p,k,Ts]=zpkdata(Msys)
% calculation of all transfer function
Hz=tf(Msys)
\end{verbatim}
%% Simulation
N = 100; % 100 sample intervals
sim('PDCBuckd1',N); % Discrete time model without saturation
sim('PDCBuckd2',N); % Discrete time model with saturation
sim('PDCBuckcl',N*h); % Continuous time model
sim('PDCBuckswitch',N*h); % PWM model with saturation

plot
figure(2)
stairs([y1 d1])
legend('Vout','d')
title('Discrete model')
xlabel('Sampling sequence k'),ylabel('Output voltage')

figure(3)
td=0:h:h*(length(y1)-1);
stairs(td,y1)
hold on, stairs(td,d1),hold off
legend('Vout','d')
title('Discrete model without saturation')
xlabel('Time s'),ylabel('Output voltage')

figure(4)
stairs([y2 d2])
legend('Vout','d')
title('Sampling sequence k'),ylabel('Output voltage')

figure(5)
plot(t,yc1)
dt=t(end)/(length(dc1)-1);

plot
figure(6)
grid

figure(7)
plot(ts,Vout,ts(1:length(d)),d,ts,pwm)
legend('Vout','d','PWM')
grid

figure(8)
plot(ts(1:1001),dVg(1:1001)*12,ts(1:1001),d(1:1001),ts(1:1001),pwm(1:1001))
legend('dVg','d','PWM')
title('Discrete PD controlled Buck, switched continuous model with saturation')
xlabel('Time'),ylabel('controlled input voltage, controller output, PWM wave')

figure(9)
plot(ts(1:2001),d(1:2001),ts(1:2001),pwm(1:2001))
legend('d','PWM')
title('Controller output & PWM wave')
xlabel('Time'),ylabel('Controller output, PWM wave')

figure(10)
plot(ts(1:10000),Noise(1:10000))
title('Noise Magnitude')
xlabel('Time'),ylabel('Noise')

figure(11)
plot(ts(1:10000),Vout_N(1:10000))
legend('With Noise')
title('Output voltage measurement with noise, without disturbance')
xlabel('Time'),ylabel('Measurement Noise')